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 */
```

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```
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 * University of Illinois at Urbana-Champaign
 * 605 E. Springfield, Champaign, IL 61820
 * httpd@ncsa.uiuc.edu
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 */
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```
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Written by Adam Twiss (adam@zeus.co.uk). March 1996

Thanks to the following people for their input:  
Mike Belshe (mbelshe@netscape.com)  
Michael Campanella (campanella@stevms.enet.dec.com)

\*/

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From strings/apr\\_strnatcmp.c, include/apr\\_strings.h:

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```
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Licenses of module/program 'cgi\\_upload':

```
-----  
  
// Programa de gestion de subidas de archivos a un servidor web  
// basado en:  
/*  
  
upload.exe -- Upload a file to a server using forms.
```

### DESCRIPTION

This is a CGI program to upload one or more files to a WWW server, using standard HTML forms instead of FTP. It works with Netscape 3.0 and 4.0, and Internet Explorer 4.0.

See the manpage for more information.

### AUTHOR

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Tel: +31(0)654 744 702

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Jeroen C. Kessels  
9 december 2000

### VERSION 2.6

```
*/  
Mas informaci?n: https://kessels.biz/upload/index.html
```

```
***** ===== *****
```

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```
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```

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\* curve25519-donna: Curve25519 elliptic curve, public key function

\* <http://code.google.com/p/curve25519-donna/>

\* Adam Langley

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```
* http://cr.yip.to/ecdh.html
*
* djb's sample implementation of curve25519 is written in a special assembly
* language called qasm and uses the floating point registers.
*
* This is, almost, a clean room reimplementation from the curve25519 paper. It
* uses many of the tricks described therein. Only the crecip function is taken
* from the sample implementation.
*/
```

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5. A program that contains no derivative of any portion of the Library, but is designed to work with the Library by being compiled or linked with it, is called a "work that uses the Library". Such a work, in isolation, is not a derivative work of the Library, and therefore falls outside the scope of this License.

However, linking a "work that uses the Library" with the Library creates an executable that is a derivative of the Library (because it contains portions of the Library), rather than a "work that uses the library". The executable is therefore covered by this License. Section 6 states terms for distribution of such executables.

When a "work that uses the Library" uses material from a header file that is part of the Library, the object code for the work may be a derivative work of the Library even though the source code is not. Whether this is true is especially significant if the work can be linked without the Library, or if the work is itself a library. The threshold for this to be true is not precisely defined by law.

If such an object file uses only numerical parameters, data structure layouts and accessors, and small macros and small inline functions (ten lines or less in length), then the use of the object file is unrestricted, regardless of whether it is legally a derivative work. (Executables containing this object code plus portions of the Library will still fall under Section 6.)

Otherwise, if the work is a derivative of the Library, you may distribute the object code for the work under the terms of Section 6. Any executables containing that work also fall under Section 6, whether or not they are linked directly with the Library itself.  
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b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is



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This option is useful when you wish to copy part of the code of the Library into a program that is not a library.

4. You may copy and distribute the Library (or a portion or derivative of it, under Section 2) in object code or executable form under the terms of Sections 1 and 2 above provided that you accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange.

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-- \$Id: README,v 1.23 2006/04/22 22:19:37 tom Exp \$
-----
README file for the ncurses package

See the file ANNOUNCE for a summary of ncurses features and ports.
See the file INSTALL for instructions on how to build and install ncurses.
See the file NEWS for a release history and bug-fix notes.
See the file TO-DO for things that still need doing, including known bugs.

Browse the file misc/ncurses-intro.html for narrative descriptions of how
to use ncurses and the panel, menu, and form libraries.

Browse the file doc/html/hackguide.html for a tour of the package internals.

ROADMAP AND PACKAGE OVERVIEW:

You should be reading this file in a directory called: ncurses-d.d, where d.d
is the current version number (see the dist.mk file in this directory for
that). There should be a number of subdirectories, including 'c++', 'form',
'man', 'menu', 'misc', 'ncurses', 'panel', 'progs', 'test', 'tack' and 'Ada95'.
(The 'tack' program may be distributed separately).

A full build/install of this package typically installs several libraries, a
handful of utilities, and a database hierarchy. Here is an inventory of the
pieces:

The libraries are:

libncurses.a      (normal)
libncurses.so     (shared)
libncurses\_g.a   (debug and trace code enabled)
libncurses\_p.a   (profiling enabled)

libpanel.a        (normal)
libpanel.so       (shared)
libpanel\_g.a     (debug and trace code enabled)

libmenu.a         (normal)
libmenu.so        (shared)
libmenu\_g.a      (debug enabled)

libform.a         (normal)
```





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libform.so (shared)  
libform\\_g.a (debug enabled)

If you configure using the `--enable-widec` option, a `'w'` is appended to the library names (e.g., `libncursesw.a`), and the resulting libraries support wide-characters, e.g., via a UTF-8 locale. The corresponding header files are compatible with the non-wide-character configuration; wide-character features are provided by `ifdef`'s in the header files. The wide-character library interfaces are not binary-compatible with the non-wide-character version.

The ncurses libraries implement the curses API. The panel, menu and forms libraries implement clones of the SVr4 panel, menu and forms APIs. The source code for these lives in the `'ncurses'`, `'panel'`, `'menu'`, and `'form'` directories respectively.

In the `'c++'` directory, you'll find code that defines an interface to the curses, forms, menus and panels library packaged as C++ classes, and a demo program in C++ to test it. These class definition modules are not installed by the `'make install.libs'` rule as `libncurses++`.

In the `'Ada95'` directory, you'll find code and documentation for an Ada95 binding of the curses API, to be used with the GNAT compiler. This binding is built by a normal top-level `'make'` if configure detects an usable version of GNAT (3.11 or above). It is not installed automatically. See the Ada95 directory for more build and installation instructions and for documentation of the binding.

To do its job, the ncurses code needs your terminal type to be set in the environment variable `TERM` (normally set by your OS; under UNIX, `getty(1)` typically does this, but you can override it in your `.profile`); and, it needs a database of terminal descriptions in which to look up your terminal type's capabilities.

In older (V7/BSD) versions of curses, the database was a flat text file, `/etc/termcap`; in newer (USG/USL) versions, the database is a hierarchy of fast-loading binary description blocks under `/usr/lib/terminfo`. These binary blocks are compiled from an improved editable text representation called `'terminfo'` format (documented in `man/terminfo.5`). The ncurses library can use either `/etc/termcap` or the compiled binary `terminfo` blocks, but prefers the second form.

In the `'misc'` directory, there is a text file `terminfo.src`, in editable `terminfo` format, which can be used to generate the `terminfo` binaries (that's what `make install.data` does). If the package was built with the `--enable-termcap` option enabled, and the ncurses library cannot find a `terminfo` description for your terminal, it will fall back to the `termcap` file supplied with your system (which the ncurses package installation leaves strictly alone).

The utilities are as follows:

```
tic          -- terminfo source to binary compiler
infocmp      -- terminfo binary to source decompiler/comparator
clear        -- emits clear-screen for current terminal
tput        -- shell-script access to terminal capabilities.
toe -- table of entries utility
tset         -- terminal-initialization utility
```

The first two (`tic` and `infocmp`) are used for manipulating `terminfo` descriptions; the next two (`clear` and `tput`) are for use in shell scripts. The last (`tset`) is provided for 4.4BSD compatibility. The source code for all of these lives in the `'progs'` directory.

Detailed documentation for all libraries and utilities can be found in the `'man'` and `'doc'` directories. An HTML introduction to ncurses, panels, and menus programming lives in the `'doc/html'` directory. Manpages in HTML format are under `'doc/html/man'`.

The `'test'` directory contains programs that can be used to verify or demonstrate the functions of the ncurses libraries. See `test/README` for descriptions of these programs. Notably, the `'ncurses'` utility is designed to help you systematically exercise the library functions.

AUTHORS:

Pavel Curtis:



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wrote the original ncurses

Zeyd M. Ben-Halim:  
port of original to Linux and many enhancements.

Thomas Dickey (maintainer for 1.9.9g through 4.1, resuming with FSF's 5.0):  
configuration scripts, porting, mods to adhere to XSI Curses in the  
areas of background color, terminal modes. Also memory leak testing,  
the wresize, default colors and key definition extensions and numerous  
bug fixes (more than half of those enumerated in NEWS beginning with  
the internal release 1.8.9).

Florian La Roche (official maintainer for FSF's ncurses 4.2)  
Beginning with release 4.2, ncurses is distributed under an MIT-style  
license.

Eric S. Raymond:  
the man pages, infocmp(1), tput(1), clear(1), captinfo(1), tset(1),  
toe(1), most of tic(1), trace levels, the HTML intro, wgetnstr() and  
many other entry points, the cursor-movement optimization, the  
scroll-pack optimizer for vertical motions, the mouse interface and  
xterm mouse support, and the ncurses test program.

Juergen Pfeifer  
The menu and form libraries, C++ bindings for ncurses, menus, forms and  
panels, as well as the Ada95 binding. Ongoing support for panel.

### CONTRIBUTORS:

Alexander V. Lukyanov  
for numerous fixes and improvements to the optimization logic.

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Jonathan Ross,  
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Keith Bostic (maintainer of 4.4BSD curses)  
for help, criticism, comments, bug-finding, and being willing to  
deep-six BSD curses for this one when it grew up.

Richard Stallman,  
for his commitment to making ncurses free software.

Countless other people have contributed by reporting bugs, sending fixes,  
suggesting improvements, and generally whining about ncurses :-)

BUGS:  
See the INSTALL file for bug and developer-list addresses.  
The Hacker's Guide in the doc directory includes some guidelines  
on how to report bugs in ways that will get them fixed most quickly.

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### PCRE JUST-IN-TIME COMPILATION SUPPORT

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Written by: Zoltan Herczeg



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```
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```

```
, 1 April 1989
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Source code files were moved around in v0.6.x releases and compared to earlier releases, the programs are now built by first going to a subdirectory (wpa\\_supplicant or hostapd) and creating build configuration (.config) and running 'make' there (for Linux/BSD/cygwin builds).

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zlib 1.2.8 is a general purpose data compression library. All the code is thread safe. The data format used by the zlib library is described by RFCs (Request for Comments) 1950 to 1952 in the files <http://tools.ietf.org/html/rfc1950> (zlib format), rfc1951 (deflate format) and rfc1952 (gzip format).

All functions of the compression library are documented in the file `zlib.h` (volunteer to write man pages welcome, contact [zlib@gzip.org](mailto:zlib@gzip.org)). A usage example of the library is given in the file `test/example.c` which also tests that the library is working correctly. Another example is given in the file `test/minigzip.c`. The compression library itself is composed of all source files in the root directory.

To compile all files and run the test program, follow the instructions given at the top of `Makefile.in`. In short `./configure; make test`, and if that goes well, `'make install'` should work for most flavors of Unix. For Windows, use one of the special makefiles in `win32/` or `contrib/vstudio/`. For VMS, use `make\_vms.com`.

Questions about zlib should be sent to `<mailto:zlib@gzip.org>`, or to Gilles Vollant for the Windows DLL version. The zlib home page is <http://zlib.net/>. Before reporting a problem, please check this site to verify that you have the latest version of zlib; otherwise get the latest version and check whether the problem still exists or not.

PLEASE read the zlib FAQ [http://zlib.net/zlib\\\_faq.html](http://zlib.net/zlib\_faq.html) before asking for help.

Mark Nelson wrote an article about zlib for the Jan. 1997 issue of Dr. Dobbs's Journal; a copy of the article is available at <http://marknelson.us/1997/01/01/zlib-engine/>.

The changes made in version 1.2.8 are documented in the file `ChangeLog`.

Unsupported third party contributions are provided in directory `contrib/`.

zlib is available in Java using the `java.util.zip` package, documented at <http://java.sun.com/developer/technicalArticles/Programming/compression/>.

A Perl interface to zlib written by Paul Marquess is available at CPAN (Comprehensive Perl Archive Network) sites, including <http://search.cpan.org/~textasciitilde{p}mq/IO-Compress-Zlib/>.

A Python interface to zlib written by A.M. Kuchling is available in Python 1.5 and later versions, see <http://docs.python.org/library/zlib.html>.

zlib is built into tcl: <http://wiki.tcl.tk/4610>.

An experimental package to read and write files in `.zip` format, written on top of zlib by Gilles Vollant, is available in the `contrib/minizip` directory of zlib.

Notes for some targets:

- For Windows DLL versions, please see `win32/DLL\_FAQ.txt`
- For 64-bit Irix, `deflate.c` must be compiled without any optimization. With `-O`, one `libpng` test fails. The test works in 32 bit mode (with the `-n32` compiler flag). The compiler bug has been reported to SGI.
- zlib doesn't work with gcc 2.6.3 on a DEC 3000/300LX under OSF/1 2.1 it works when compiled with `cc`.
- On Digital Unix 4.0D (formerly OSF/1) on AlphaServer, the `cc` option `-std1` is necessary to get `gzprintf` working correctly. This is done by `configure`.
- zlib doesn't work on HP-UX 9.05 with some versions of `/bin/cc`. It works with



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other compilers. Use 'make test' to check your compiler.

- gzdopen is not supported on RISCOS or BEOS.
- For PalmOs, see <http://palmzlib.sourceforge.net/>

### Acknowledgments:

The deflate format used by zlib was defined by Phil Katz. The deflate and zlib specifications were written by L. Peter Deutsch. Thanks to all the people who reported problems and suggested various improvements in zlib; they are too numerous to cite here.

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```
-----
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\#
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\# -pPATHNAME
\#   Look in PATHNAME instead of \${PATH}
\#
\# Usage:
\# PrintPath {\lbrack}-s{\rbrack} {\lbrack}-pPATHNAME{\rbrack} program {\lbrack}program ...{\rbrack}
\#
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Component gcc/libbacktrace:  
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The libbacktrace library  
Initially written by Ian Lance Taylor

The libbacktrace library may be linked into a program or library and used to produce symbolic backtraces. Sample uses would be to print a detailed backtrace when an error occurs or to gather detailed profiling information.

The libbacktrace library is provided under a BSD license. See the source files for the exact license text.

The public functions are declared and documented in the header file backtrace.h, which should be `#include'd` by a user of the library.

Building libbacktrace will generate a file backtrace-supported.h, which a user of the library may use to determine whether backtraces will work. See the source file backtrace-supported.h.in for the macros that it defines.

As of September 2012, libbacktrace only supports ELF executables with DWARF debugging information. The library is written to make it straightforward to add support for other object file and debugging formats.

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From musl src/string/strverscmp.c

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@(\#)filio.h      8.1 (Berkeley) 3/28/94
\ FreeBSD: head/sys/sys/filio.h 314436 2017-02-28 23:42:47Z imp \
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\#  
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```

File expat-2.1.1/confertools/PrintPath:

```
-----  
\# Look for program{\lbrack}s{\rbrack} somewhere in \${PATH}.  
\#  
\# Options:  
\# -s  
\# Do not print out full pathname. (silent)  
\# -pPATHNAME  
\# Look in PATHNAME instead of \${PATH}  
\#  
\# Usage:  
\# PrintPath {\lbrack}-s{\rbrack} {\lbrack}-pPATHNAME{\rbrack} program {\lbrack}program ...{\rbrack}  
\#  
\# Initially written by Jim Jagielski for the Apache configuration mechanism
```





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```

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```

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```

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```
Version 3.1, 31 March 2009
```

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```
0. Definitions.
```

```
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Component gcc/libbacktrace:  
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The libbacktrace library  
Initially written by Ian Lance Taylor

The libbacktrace library may be linked into a program or library and used to produce symbolic backtraces. Sample uses would be to print a detailed backtrace when an error occurs or to gather detailed profiling information.

The libbacktrace library is provided under a BSD license. See the source files for the exact license text.

The public functions are declared and documented in the header file backtrace.h, which should be `#include'd` by a user of the library.

Building libbacktrace will generate a file backtrace-supported.h, which a user of the library may use to determine whether backtraces will work. See the source file backtrace-supported.h.in for the macros that it defines.

As of September 2012, libbacktrace only supports ELF executables with DWARF debugging information. The library is written to make it straightforward to add support for other object file and debugging formats.

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From musl include/elf.h

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From musl src/string/strverscmp.c

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```
@(\#)filio.h      8.1 (Berkeley) 3/28/94
\ FreeBSD: head/sys/sys/filio.h 314436 2017-02-28 23:42:47Z imp \
```

(57)  
Configuration for math routines.  
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bits/byteswap.h

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```

Summary:  
=====

This directory contains the source code for U-Boot, a boot loader for Embedded boards based on PowerPC, ARM, MIPS and several other processors, which can be installed in a boot ROM and used to initialize and test the hardware or to download and run application code.

The development of U-Boot is closely related to Linux: some parts of the source code originate in the Linux source tree, we have some header files in common, and special provision has been made to support booting of Linux images.

Some attention has been paid to make this software easily configurable and extendable. For instance, all monitor commands are implemented with the same call interface, so that it's very easy to add new commands. Also, instead of permanently adding rarely used code (for instance hardware test utilities) to the monitor, you can load and run it dynamically.

Status:  
=====

In general, all boards for which a configuration option exists in the Makefile have been tested to some extent and can be considered 'working'. In fact, many of them are used in production systems.

In case of problems see the CHANGELOG file to find out who contributed the specific port. In addition, there are various MAINTAINERS files scattered throughout the U-Boot source identifying the people or companies responsible for various boards and subsystems.

Note: As of August, 2010, there is no longer a CHANGELOG file in the actual U-Boot source tree; however, it can be created dynamically from the Git log using:

```
make CHANGELOG
```

Where to get help:  
=====

In case you have questions about, problems with or contributions for U-Boot, you should send a message to the U-Boot mailing list at [u-boot@denx.de](mailto:u-boot@denx.de). There is also an archive of previous traffic on the mailing list - please search the archive before asking FAQ's. Please see <https://lists.denx.de/pipermail/u-boot> and



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<https://marc.info/?l=u-boot>

Where to get source code:

=====

The U-Boot source code is maintained in the Git repository at  
<https://source.denx.de/u-boot/u-boot.git> ; you can browse it online at  
<https://source.denx.de/u-boot/u-boot>

The ''Tags'' links on this page allow you to download tarballs of any version you might be interested in. Official releases are also available from the DENX file server through HTTPS or FTP.

<https://ftp.denx.de/pub/u-boot/>  
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Where we come from:

=====

- start from 8xxrom sources
- create PPCBoot project (<https://sourceforge.net/projects/ppcboot>)
- clean up code
- make it easier to add custom boards
- make it possible to add other {\lbrack}PowerPC{\rbrack} CPUs
- extend functions, especially:
  - \* Provide extended interface to Linux boot loader
  - \* S-Record download
  - \* network boot
  - \* ATA disk / SCSI ... boot
- create ARMBoot project (<https://sourceforge.net/projects/armboot>)
- add other CPU families (starting with ARM)
- create U-Boot project (<https://sourceforge.net/projects/u-boot>)
- current project page: see <https://www.denx.de/wiki/U-Boot>

Names and Spelling:

=====

The ''official'' name of this project is ''Das U-Boot''. The spelling ''U-Boot'' shall be used in all written text (documentation, comments in source files etc.). Example:

This is the README file for the U-Boot project.

File names etc. shall be based on the string ''u-boot''. Examples:

```
include/asm-ppc/u-boot.h
```

```
\#include
```

Variable names, preprocessor constants etc. shall be either based on the string ''u\\_boot'' or on ''U\\_BOOT''. Example:

```
U\_BOOT\_VERSION u\_boot\_logo  
IH\_OS\_U\_BOOT u\_boot\_hush\_start
```

Versioning:

=====

Starting with the release in October 2008, the names of the releases were changed from numerical release numbers without deeper meaning into a time stamp based numbering. Regular releases are identified by names consisting of the calendar year and month of the release date. Additional fields (if present) indicate release candidates or bug fix releases in ''stable'' maintenance trees.

Examples:

U-Boot v2009.11 - Release November 2009  
U-Boot v2009.11.1 - Release 1 in version November 2009 stable tree  
U-Boot v2010.09-rc1 - Release candidate 1 for September 2010 release

Directory Hierarchy:

=====

/arch Architecture specific files

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```
/arc Files generic to ARC architecture
/arm Files generic to ARM architecture
/m68k Files generic to m68k architecture
/microblaze Files generic to microblaze architecture
/mips Files generic to MIPS architecture
/nds32 Files generic to NDS32 architecture
/nios2 Files generic to Altera NIOS2 architecture
/powerpc Files generic to PowerPC architecture
/riscv Files generic to RISC-V architecture
/sandbox Files generic to HW-independent 'sandbox'
/sh Files generic to SH architecture
/x86 Files generic to x86 architecture
/xtensa Files generic to Xtensa architecture
/api Machine/arch independent API for external apps
/board Board dependent files
/cmd U-Boot commands functions
/common Misc architecture independent functions
/configs Board default configuration files
/disk Code for disk drive partition handling
/doc Documentation (don't expect too much)
/drivers Commonly used device drivers
/dts Contains Makefile for building internal U-Boot fdt.
/env Environment files
/examples Example code for standalone applications, etc.
/fs Filesystem code (cramfs, ext2, jffs2, etc.)
/include Header Files
/lib Library routines generic to all architectures
/Licenses Various license files
/net Networking code
/post Power On Self Test
/scripts Various build scripts and Makefiles
/test Various unit test files
/tools Tools to build S-Record or U-Boot images, etc.
```

Software Configuration:

=====

Configuration is usually done using C preprocessor defines; the rationale behind that is to avoid dead code whenever possible.

There are two classes of configuration variables:

\* Configuration \\_OPTIONS\\_:

These are selectable by the user and have names beginning with 'CONFIG\\_'.

\* Configuration \\_SETTINGS\\_:

These depend on the hardware etc. and should not be meddled with if you don't know what you're doing; they have names beginning with 'CONFIG\\_SYS\\_'.

Previously, all configuration was done by hand, which involved creating symbolic links and editing configuration files manually. More recently, U-Boot has added the Kbuild infrastructure used by the Linux kernel, allowing you to use the 'make menuconfig' command to configure your build.

Selection of Processor Architecture and Board Type:

-----

For all supported boards there are ready-to-use default configurations available; just type 'make \\_defconfig'.

Example: For a TQM823L module type:

```
cd u-boot
make TQM823L\_defconfig
```

Note: If you're looking for the default configuration file for a board you're sure used to be there but is now missing, check the file doc/README.scrapyard for a list of no longer supported boards.

Sandbox Environment:

-----

U-Boot can be built natively to run on a Linux host using the 'sandbox'





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board. This allows feature development which is not board- or architecture-specific to be undertaken on a native platform. The sandbox is also used to run some of U-Boot's tests.

See doc/arch/sandbox.rst for more details.

Board Initialisation Flow:

-----  
This is the intended start-up flow for boards. This should apply for both SPL and U-Boot proper (i.e. they both follow the same rules).

Note: 'SPL' stands for 'Secondary Program Loader,' which is explained in more detail later in this file.

At present, SPL mostly uses a separate code path, but the function names and roles of each function are the same. Some boards or architectures may not conform to this. At least most ARM boards which use CONFIG\_SPL\_FRAMEWORK conform to this.

Execution typically starts with an architecture-specific (and possibly CPU-specific) start.S file, such as:

- arch/arm/cpu/armv7/start.S
- arch/powerpc/cpu/mpc83xx/start.S
- arch/mips/cpu/start.S

and so on. From there, three functions are called; the purpose and limitations of each of these functions are described below.

lowlevel\_init():

- purpose: essential init to permit execution to reach board\_init\_f()
- no global\_data or BSS
- there is no stack (ARMv7 may have one but it will soon be removed)
- must not set up SDRAM or use console
- must only do the bare minimum to allow execution to continue to board\_init\_f()
- this is almost never needed
- return normally from this function

board\_init\_f():

- purpose: set up the machine ready for running board\_init\_r(): i.e. SDRAM and serial UART
- global\_data is available
- stack is in SRAM
- BSS is not available, so you cannot use global/static variables, only stack variables and global\_data

Non-SPL-specific notes:

- dram\_init() is called to set up DRAM. If already done in SPL this can do nothing

SPL-specific notes:

- you can override the entire board\_init\_f() function with your own version as needed.
- preloader\_console\_init() can be called here in extremis
- should set up SDRAM, and anything needed to make the UART work
- there is no need to clear BSS, it will be done by crt0.S
- for specific scenarios on certain architectures an early BSS \*can\* be made available (via CONFIG\_SPL\_EARLY\_BSS by moving the clearing of BSS prior to entering board\_init\_f()) but doing so is discouraged. Instead it is strongly recommended to architect any code changes or additions such to not depend on the availability of BSS during board\_init\_f() as indicated in other sections of this README to maintain compatibility and consistency across the entire code base.
- must return normally from this function (don't call board\_init\_r() directly)

Here the BSS is cleared. For SPL, if CONFIG\_SPL\_STACK\_R is defined, then at this point the stack and global\_data are relocated to below CONFIG\_SPL\_STACK\_R\_ADDR. For non-SPL, U-Boot is relocated to run at the top of memory.

board\_init\_r():

- purpose: main execution, common code
- global\_data is available



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- SDRAM is available
- BSS is available, all static/global variables can be used
- execution eventually continues to main\\_loop()

#### Non-SPL-specific notes:

- U-Boot is relocated to the top of memory and is now running from there.

#### SPL-specific notes:

- stack is optionally in SDRAM, if CONFIG\\_SPL\\_STACK\\_R is defined and CONFIG\\_SPL\\_STACK\\_R\\_ADDR points into SDRAM
- preloader\\_console\\_init() can be called here - typically this is done by selecting CONFIG\\_SPL\\_BOARD\\_INIT and then supplying a spl\\_board\\_init() function containing this call
- loads U-Boot or (in falcon mode) Linux

#### Configuration Options:

-----  
Configuration depends on the combination of board and CPU type; all such information is kept in a configuration file  
''include/configs/.h''.

Example: For a TQM823L module, all configuration settings are in  
''include/configs/TQM823L.h''.

Many of the options are named exactly as the corresponding Linux kernel configuration options. The intention is to make it easier to build a config tool - later.

- ARM Platform Bus Type(CCI):  
CoreLink Cache Coherent Interconnect (CCI) is ARM BUS which provides full cache coherency between two clusters of multi-core CPUs and I/O coherency for devices and I/O masters

CONFIG\\_SYS\\_FSL\\_HAS\\_CCI400

Defined For SoC that has cache coherent interconnect  
CCN-400

CONFIG\\_SYS\\_FSL\\_HAS\\_CCN504

Defined for SoC that has cache coherent interconnect CCN-504

The following options need to be configured:

- CPU Type: Define exactly one, e.g. CONFIG\\_MPC85XX.
- Board Type: Define exactly one, e.g. CONFIG\\_MPC8540ADS.

- 85xx CPU Options:

CONFIG\\_SYS\\_PPC64

Specifies that the core is a 64-bit PowerPC implementation (implements the ''64'' category of the Power ISA). This is necessary for ePAPR compliance, among other possible reasons.

CONFIG\\_SYS\\_FSL\\_TBCLK\\_DIV

Defines the core time base clock divider ratio compared to the system clock. On most PQ3 devices this is 8, on newer QorIQ devices it can be 16 or 32. The ratio varies from SoC to Soc.

CONFIG\\_SYS\\_FSL\\_PCIE\\_COMPAT

Defines the string to utilize when trying to match PCIe device tree nodes for the given platform.

CONFIG\\_SYS\\_FSL\\_ERRATUM\\_A004510

Enables a workaround for erratum A004510. If set, then CONFIG\\_SYS\\_FSL\\_ERRATUM\\_A004510\\_SVR\\_REV and CONFIG\\_SYS\\_FSL\\_CORENET\\_SNOOPVEC\\_COREONLY must be set.



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CONFIG\\_SYS\\_FSL\\_ERRATUM\\_A004510\\_SVR\\_REV  
CONFIG\\_SYS\\_FSL\\_ERRATUM\\_A004510\\_SVR\\_REV2 (optional)

Defines one or two SoC revisions (low 8 bits of SVR) for which the A004510 workaround should be applied.

The rest of SVR is either not relevant to the decision of whether the erratum is present (e.g. p2040 versus p2041) or is implied by the build target, which controls whether CONFIG\\_SYS\\_FSL\\_ERRATUM\\_A004510 is set.

See Freescale App Note 4493 for more information about this erratum.

CONFIG\\_A003399\\_NOR\\_WORKAROUND  
Enables a workaround for IFC erratum A003399. It is only required during NOR boot.

CONFIG\\_A008044\\_WORKAROUND  
Enables a workaround for T1040/T1042 erratum A008044. It is only required during NAND boot and valid for Rev 1.0 SoC revision

CONFIG\\_SYS\\_FSL\\_CORENET\\_SNOOPVEC\\_COREONLY

This is the value to write into CCSR offset 0x18600 according to the A004510 workaround.

CONFIG\\_SYS\\_FSL\\_DSP\\_DDR\\_ADDR  
This value denotes start offset of DDR memory which is connected exclusively to the DSP cores.

CONFIG\\_SYS\\_FSL\\_DSP\\_M2\\_RAM\\_ADDR  
This value denotes start offset of M2 memory which is directly connected to the DSP core.

CONFIG\\_SYS\\_FSL\\_DSP\\_M3\\_RAM\\_ADDR  
This value denotes start offset of M3 memory which is directly connected to the DSP core.

CONFIG\\_SYS\\_FSL\\_DSP\\_CCSRBAR\\_DEFAULT  
This value denotes start offset of DSP CCSR space.

CONFIG\\_SYS\\_FSL\\_SINGLE\\_SOURCE\\_CLK  
Single Source Clock is clocking mode present in some of FSL SoC's. In this mode, a single differential clock is used to supply clocks to the sysclock, ddrclk and usbclock.

CONFIG\\_SYS\\_CPC\\_REINIT\\_F  
This CONFIG is defined when the CPC is configured as SRAM at the time of U-Boot entry and is required to be re-initialized.

CONFIG\\_DEEP\\_SLEEP  
Indicates this SoC supports deep sleep feature. If deep sleep is supported, core will start to execute uboot when wakes up.

- Generic CPU options:  
CONFIG\\_SYS\\_BIG\\_ENDIAN, CONFIG\\_SYS\\_LITTLE\\_ENDIAN

Defines the endianness of the CPU. Implementation of those values is arch specific.

CONFIG\\_SYS\\_FSL\\_DDR  
Freescale DDR driver in use. This type of DDR controller is found in mpc83xx, mpc85xx, mpc86xx as well as some ARM core SoCs.

CONFIG\\_SYS\\_FSL\\_DDR\\_ADDR  
Freescale DDR memory-mapped register base.

CONFIG\\_SYS\\_FSL\\_DDR\\_EMU  
Specify emulator support for DDR. Some DDR features such as deskew training are not available.

CONFIG\\_SYS\\_FSL\\_DDRC\\_GEN1  
Freescale DDR1 controller.

CONFIG\\_SYS\\_FSL\\_DDRC\\_GEN2



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Freescale DDR2 controller.

CONFIG\\_SYS\\_FSL\\_DDRC\\_GEN3  
Freescale DDR3 controller.

CONFIG\\_SYS\\_FSL\\_DDRC\\_GEN4  
Freescale DDR4 controller.

CONFIG\\_SYS\\_FSL\\_DDRC\\_ARM\\_GEN3  
Freescale DDR3 controller for ARM-based SoCs.

CONFIG\\_SYS\\_FSL\\_DDR1  
Board config to use DDR1. It can be enabled for SoCs with Freescale DDR1 or DDR2 controllers, depending on the board implementation.

CONFIG\\_SYS\\_FSL\\_DDR2  
Board config to use DDR2. It can be enabled for SoCs with Freescale DDR2 or DDR3 controllers, depending on the board implementation.

CONFIG\\_SYS\\_FSL\\_DDR3  
Board config to use DDR3. It can be enabled for SoCs with Freescale DDR3 or DDR3L controllers.

CONFIG\\_SYS\\_FSL\\_DDR3L  
Board config to use DDR3L. It can be enabled for SoCs with DDR3L controllers.

CONFIG\\_SYS\\_FSL\\_DDR4  
Board config to use DDR4. It can be enabled for SoCs with DDR4 controllers.

CONFIG\\_SYS\\_FSL\\_IFC\\_BE  
Defines the IFC controller register space as Big Endian

CONFIG\\_SYS\\_FSL\\_IFC\\_LE  
Defines the IFC controller register space as Little Endian

CONFIG\\_SYS\\_FSL\\_IFC\\_CLK\\_DIV  
Defines divider of platform clock(clock input to IFC controller).

CONFIG\\_SYS\\_FSL\\_LBC\\_CLK\\_DIV  
Defines divider of platform clock(clock input to eLBC controller).

CONFIG\\_SYS\\_FSL\\_PBL\\_PBI  
It enables addition of RCW (Power on reset configuration) in built image. Please refer doc/README.pblimage for more details

CONFIG\\_SYS\\_FSL\\_PBL\\_RCW  
It adds PBI(pre-boot instructions) commands in u-boot build image. PBI commands can be used to configure SoC before it starts the execution. Please refer doc/README.pblimage for more details

CONFIG\\_SYS\\_FSL\\_DDR\\_BE  
Defines the DDR controller register space as Big Endian

CONFIG\\_SYS\\_FSL\\_DDR\\_LE  
Defines the DDR controller register space as Little Endian

CONFIG\\_SYS\\_FSL\\_DDR\\_SDRAM\\_BASE\\_PHY  
Physical address from the view of DDR controllers. It is the same as CONFIG\\_SYS\\_DDR\\_SDRAM\\_BASE for all Power SoCs. But it could be different for ARM SoCs.

CONFIG\\_SYS\\_FSL\\_DDR\\_INTLV\\_256B  
DDR controller interleaving on 256-byte. This is a special interleaving mode, handled by Dickens for Freescale layerscape SoCs with ARM core.

CONFIG\\_SYS\\_FSL\\_DDR\\_MAIN\\_NUM\\_CTRLS  
Number of controllers used as main memory.

CONFIG\\_SYS\\_FSL\\_OTHER\\_DDR\\_NUM\\_CTRLS  
Number of controllers used for other than main memory.

CONFIG\\_SYS\\_FSL\\_HAS\\_DP\\_DDR



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Defines the SoC has DP-DDR used for DPAA.

CONFIG\\_SYS\\_FSL\\_SEC\\_BE

Defines the SEC controller register space as Big Endian

CONFIG\\_SYS\\_FSL\\_SEC\\_LE

Defines the SEC controller register space as Little Endian

- MIPS CPU options:

CONFIG\\_SYS\\_INIT\\_SP\\_OFFSET

Offset relative to CONFIG\\_SYS\\_SDRAM\\_BASE for initial stack pointer. This is needed for the temporary stack before relocation.

CONFIG\\_XWAY\\_SWAP\\_BYTES

Enable compilation of tools/xway-swap-bytes needed for Lantiq XWAY SoCs for booting from NOR flash. The U-Boot image needs to be swapped if a flash programmer is used.

- ARM options:

CONFIG\\_SYS\\_EXCEPTION\\_VECTORS\\_HIGH

Select high exception vectors of the ARM core, e.g., do not clear the V bit of the cl register of CP15.

COUNTER\\_FREQUENCY

Generic timer clock source frequency.

COUNTER\\_FREQUENCY\\_REAL

Generic timer clock source frequency if the real clock is different from COUNTER\\_FREQUENCY, and can only be determined at run time.

- Tegra SoC options:

CONFIG\\_TEGRA\\_SUPPORT\\_NON\\_SECURE

Support executing U-Boot in non-secure (NS) mode. Certain impossible actions will be skipped if the CPU is in NS mode, such as ARM architectural timer initialization.

- Linux Kernel Interface:

CONFIG\\_MEMSIZE\\_IN\\_BYTES {\lbrack}relevant for MIPS only{\rbrack}

When transferring memsize parameter to Linux, some versions expect it to be in bytes, others in MB.

Define CONFIG\\_MEMSIZE\\_IN\\_BYTES to make it in bytes.

CONFIG\\_OF\\_LIBFDT

New kernel versions are expecting firmware settings to be passed using flattened device trees (based on open firmware concepts).

CONFIG\\_OF\\_LIBFDT

- \* New libfdt-based support
- \* Adds the 'fdt' command
- \* The bootm command automatically updates the fdt

OF\\_TBCLK - The timebase frequency.

boards with QUICC Engines require OF\\_QE to set UCC MAC addresses

CONFIG\\_OF\\_BOARD\\_SETUP

Board code has addition modification that it wants to make to the flat device tree before handing it off to the kernel

CONFIG\\_OF\\_SYSTEM\\_SETUP

Other code has addition modification that it wants to make to the flat device tree before handing it off to the kernel. This causes ft\\_system\\_setup() to be called before booting the kernel.



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CONFIG\\_OF\\_IDE\\_FIXUP

U-Boot can detect if an IDE device is present or not. If not, and this new config option is activated, U-Boot removes the ATA node from the DTS before booting Linux, so the Linux IDE driver does not probe the device and crash. This is needed for buggy hardware (uc101) where no pull down resistor is connected to the signal IDE5V\\_DD7.

CONFIG\\_MACH\\_TYPE {\lbrack}relevant for ARM only{\rbrack}{\lbrack}mandatory{\rbrack}

This setting is mandatory for all boards that have only one machine type and must be used to specify the machine type number as it appears in the ARM machine registry (see <https://www.arm.linux.org.uk/developer/machines/>). Only boards that have multiple machine types supported in a single configuration file and the machine type is runtime discoverable, do not have to use this setting.

- vxWorks boot parameters:

bootvx constructs a valid bootline using the following environments variables: bootdev, bootfile, ipaddr, netmask, serverip, gatewayip, hostname, othbootargs. It loads the vxWorks image pointed bootfile.

Note: If a ''bootargs'' environment is defined, it will override the defaults discussed just above.

- Cache Configuration:

CONFIG\\_SYS\\_L2CACHE\\_OFF- Do not enable L2 cache in U-Boot

- Cache Configuration for ARM:

CONFIG\\_SYS\\_L2\\_PL310 - Enable support for ARM PL310 L2 cache controller  
CONFIG\\_SYS\\_PL310\\_BASE - Physical base address of PL310 controller register space

- Serial Ports:

CONFIG\\_PLO10\\_SERIAL

Define this if you want support for Amba PrimeCell PLO10 UARTs.

CONFIG\\_PLO11\\_SERIAL

Define this if you want support for Amba PrimeCell PLO11 UARTs.

CONFIG\\_PLO11\\_CLOCK

If you have Amba PrimeCell PLO11 UARTs, set this variable to the clock speed of the UARTs.

CONFIG\\_PLO1x\\_PORTS

If you have Amba PrimeCell PLO10 or PLO11 UARTs on your board, define this to a list of base addresses for each (supported) port. See e.g. include/configs/versatile.h

CONFIG\\_SERIAL\\_HW\\_FLOW\\_CONTROL

Define this variable to enable hw flow control in serial driver. Current user of this option is drivers/serial/ns116550.c driver

- Autoboot Command:

CONFIG\\_BOOTCOMMAND

Only needed when CONFIG\\_BOOTDELAY is enabled; define a command string that is automatically executed when no character is read on the console interface within ''Boot Delay'' after reset.

CONFIG\\_RAMBOOT and CONFIG\\_NFSBOOT

The value of these goes into the environment as ''ramboot'' and ''nfsboot'' respectively, and can be used as a convenience, when switching between booting from RAM and NFS.

- Serial Download Echo Mode:



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### CONFIG\\_LOADS\\_ECHO

If defined to 1, all characters received during a serial download (using the 'loads' command) are echoed back. This might be needed by some terminal emulations (like 'cu'), but may as well just take time on others. This setting \#define's the initial value of the 'loads\\_echo' environment variable.

- Kgdb Serial Baudrate: (if CONFIG\\_CMD\\_KGDB is defined)  
CONFIG\\_KGDB\\_BAUDRATE  
Select one of the baudrates listed in  
CONFIG\\_SYS\\_BAUDRATE\\_TABLE, see below.

- Removal of commands  
If no commands are needed to boot, you can disable CONFIG\\_CMDLINE to remove them. In this case, the command line will not be available, and when U-Boot wants to execute the boot command (on start-up) it will call board\\_run\\_command() instead. This can reduce image size significantly for very simple boot procedures.

- Regular expression support:  
CONFIG\\_REGEX  
If this variable is defined, U-Boot is linked against the SLRE (Super Light Regular Expression) library, which adds regex support to some commands, as for example 'env grep' and 'setexpr'.

- Device tree:  
CONFIG\\_OF\\_CONTROL  
If this variable is defined, U-Boot will use a device tree to configure its devices, instead of relying on statically compiled \#defines in the board file. This option is experimental and only available on a few boards. The device tree is available in the global data as gd->fdt\\_blob.

U-Boot needs to get its device tree from somewhere. This can be done using one of the three options below:

CONFIG\\_OF\\_EMBED  
If this variable is defined, U-Boot will embed a device tree binary in its image. This device tree file should be in the board directory and called -.dts. The binary file is then picked up in board\\_init\\_f() and made available through the global data structure as gd->fdt\\_blob.

CONFIG\\_OF\\_SEPARATE  
If this variable is defined, U-Boot will build a device tree binary. It will be called u-boot.dtb. Architecture-specific code will locate it at run-time. Generally this works by:

```
cat u-boot.bin u-boot.dtb >image.bin
```

and in fact, U-Boot does this for you, creating a file called u-boot-dtb.bin which is useful in the common case. You can still use the individual files if you need something more exotic.

CONFIG\\_OF\\_BOARD  
If this variable is defined, U-Boot will use the device tree provided by the board at runtime instead of embedding one with the image. Only boards defining board\\_fdt\\_blob\\_setup() support this option (see include/fdtdec.h file).

- Watchdog:  
CONFIG\\_WATCHDOG  
If this variable is defined, it enables watchdog support for the SoC. There must be support in the SoC specific code for a watchdog. For the 8xx CPUs, the SIU Watchdog feature is enabled in the SYPCR register. When supported for a specific SoC is available, then no further board specific code should be needed to use it.

CONFIG\\_HW\\_WATCHDOG  
When using a watchdog circuitry external to the used SoC, then define this variable and provide board



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specific code for the 'hw\\_watchdog\\_reset' function.

- Real-Time Clock:

When CONFIG\\_CMD\\_DATE is selected, the type of the RTC has to be selected, too. Define exactly one of the following options:

CONFIG\\_RTC\\_PCF8563 - use Philips PCF8563 RTC  
CONFIG\\_RTC\\_MC13XXX - use MC13783 or MC13892 RTC  
CONFIG\\_RTC\\_MC146818 - use MC146818 RTC  
CONFIG\\_RTC\\_DS1307 - use Maxim, Inc. DS1307 RTC  
CONFIG\\_RTC\\_DS1337 - use Maxim, Inc. DS1337 RTC  
CONFIG\\_RTC\\_DS1338 - use Maxim, Inc. DS1338 RTC  
CONFIG\\_RTC\\_DS1339 - use Maxim, Inc. DS1339 RTC  
CONFIG\\_RTC\\_DS164x - use Dallas DS164x RTC  
CONFIG\\_RTC\\_ISL1208 - use Intersil ISL1208 RTC  
CONFIG\\_RTC\\_MAX6900 - use Maxim, Inc. MAX6900 RTC  
CONFIG\\_RTC\\_DS1337\\_NOOSC - Turn off the OSC output for DS1337  
CONFIG\\_SYS\\_RV3029\\_TCR - enable trickle charger on RV3029 RTC.

Note that if the RTC uses I2C, then the I2C interface must also be configured. See I2C Support, below.

- GPIO Support:

CONFIG\\_PCA953X - use NXP's PCA953X series I2C GPIO

The CONFIG\\_SYS\\_I2C\\_PCA953X\\_WIDTH option specifies a list of chip-ngpio pairs that tell the PCA953X driver the number of pins supported by a particular chip.

Note that if the GPIO device uses I2C, then the I2C interface must also be configured. See I2C Support, below.

- I/O tracing:

When CONFIG\\_IO\\_TRACE is selected, U-Boot intercepts all I/O accesses and can checksum them or write a list of them out to memory. See the 'iotrace' command for details. This is useful for testing device drivers since it can confirm that the driver behaves the same way before and after a code change. Currently this is supported on sandbox and arm. To add support for your architecture, add '#include' to the bottom of arch//include/asm/io.h and test.

Example output from the 'iotrace stats' command is below. Note that if the trace buffer is exhausted, the checksum will still continue to operate.

```
iotrace is enabled
Start: 10000000 (buffer start address)
Size: 00010000 (buffer size)
Offset: 00000120 (current buffer offset)
Output: 10000120 (start + offset)
Count: 00000018 (number of trace records)
CRC32: 9526fb66 (CRC32 of all trace records)
```

- Timestamp Support:

When CONFIG\\_TIMESTAMP is selected, the timestamp (date and time) of an image is printed by image commands like bootm or iminfo. This option is automatically enabled when you select CONFIG\\_CMD\\_DATE .

- Partition Labels (disklabels) Supported:

Zero or more of the following:

CONFIG\\_MAC\\_PARTITION Apple's MacOS partition table.  
CONFIG\\_ISO\\_PARTITION ISO partition table, used on CDROM etc.  
CONFIG\\_EFI\\_PARTITION GPT partition table, common when EFI is the bootloader. Note 2TB partition limit; see disk/part\\_efi.c  
CONFIG\\_SCSI you must configure support for at least one non-MTD partition type as well.

- IDE Reset method:

CONFIG\\_IDE\\_RESET\\_ROUTINE - this is defined in several board configurations files but used nowhere!





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CONFIG\\_IDE\\_RESET - is this is defined, IDE Reset will be performed by calling the function  
ide\\_set\\_reset(int reset)  
which has to be defined in a board specific file

- ATAPI Support:  
CONFIG\\_ATAPI

Set this to enable ATAPI support.

- LBA48 Support  
CONFIG\\_LBA48

Set this to enable support for disks larger than 137GB  
Also look at CONFIG\\_SYS\\_64BIT\\_LBA.  
Without these , LBA48 support uses 32bit variables and will 'only'  
support disks up to 2.1TB.

CONFIG\\_SYS\\_64BIT\\_LBA:  
When enabled, makes the IDE subsystem use 64bit sector addresses.  
Default is 32bit.

- SCSI Support:  
CONFIG\\_SYS\\_SCSI\\_MAX\\_LUN {\lbrack}8{\rbrack}, CONFIG\\_SYS\\_SCSI\\_MAX\\_SCSI\\_ID {\lbrack}7{\rbrack} and  
CONFIG\\_SYS\\_SCSI\\_MAX\\_DEVICE {\lbrack}CONFIG\\_SYS\\_SCSI\\_MAX\\_SCSI\\_ID \*  
CONFIG\\_SYS\\_SCSI\\_MAX\\_LUN{\rbrack} can be adjusted to define the  
maximum numbers of LUNs, SCSI ID's and target  
devices.

The environment variable 'scsidevs' is set to the number of  
SCSI devices found during the last scan.

- NETWORK Support (PCI):  
CONFIG\\_E1000  
Support for Intel 8254x/8257x gigabit chips.

CONFIG\\_E1000\\_SPI  
Utility code for direct access to the SPI bus on Intel 8257x.  
This does not do anything useful unless you set at least one  
of CONFIG\\_CMD\\_E1000 or CONFIG\\_E1000\\_SPI\\_GENERIC.

CONFIG\\_E1000\\_SPI\\_GENERIC  
Allow generic access to the SPI bus on the Intel 8257x, for  
example with the ''sspi'' command.

CONFIG\\_NATSEMI  
Support for National dp83815 chips.

CONFIG\\_NS8382X  
Support for National dp8382{\lbrack}01{\rbrack} gigabit chips.

- NETWORK Support (other):

CONFIG\\_DRIVER\\_AT91EMAC  
Support for AT91RM9200 EMAC.

CONFIG\\_RMII  
Define this to use reduced MII interface

CONFIG\\_DRIVER\\_AT91EMAC\\_QUIET  
If this defined, the driver is quiet.  
The driver doesn't show link status messages.

CONFIG\\_CALXEDA\\_XGMAC  
Support for the Calxeda XGMAC device

CONFIG\\_LAN91C96  
Support for SMSC's LAN91C96 chips.

CONFIG\\_LAN91C96\\_USE\\_32\\_BIT  
Define this to enable 32 bit addressing

CONFIG\\_SMC9111  
Support for SMSC's LAN91C111 chip

CONFIG\\_SMC9111\\_BASE



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Define this to hold the physical address  
of the device (I/O space)

CONFIG\\_SMC\\_USE\\_32\\_BIT  
Define this if data bus is 32 bits

CONFIG\\_SMC\\_USE\\_IOFUNCS  
Define this to use i/o functions instead of macros  
(some hardware wont work with macros)

CONFIG\\_SYS\\_DAVINCI\\_EMAC\\_PHY\\_COUNT  
Define this if you have more then 3 PHYs.

CONFIG\\_FTGMAC100  
Support for Faraday's FTGMAC100 Gigabit SoC Ethernet

CONFIG\\_FTGMAC100\\_EGIGA  
Define this to use GE link update with gigabit PHY.  
Define this if FTGMAC100 is connected to gigabit PHY.  
If your system has 10/100 PHY only, it might not occur  
wrong behavior. Because PHY usually return timeout or  
useless data when polling gigabit status and gigabit  
control registers. This behavior won't affect the  
correctness of 10/100 link speed update.

CONFIG\\_SH\\_ETHER  
Support for Renesas on-chip Ethernet controller

CONFIG\\_SH\\_ETHER\\_USE\\_PORT  
Define the number of ports to be used

CONFIG\\_SH\\_ETHER\\_PHY\\_ADDR  
Define the ETH PHY's address

CONFIG\\_SH\\_ETHER\\_CACHE\\_WRITEBACK  
If this option is set, the driver enables cache flush.

- TPM Support:  
CONFIG\\_TPM  
Support TPM devices.

CONFIG\\_TPM\\_TIS\\_INFINEON  
Support for Infineon i2c bus TPM devices. Only one device  
per system is supported at this time.

CONFIG\\_TPM\\_TIS\\_I2C\\_BURST\\_LIMITATION  
Define the burst count bytes upper limit

CONFIG\\_TPM\\_ST33ZP24  
Support for STMicroelectronics TPM devices. Requires DM\\_TPM support.

CONFIG\\_TPM\\_ST33ZP24\\_I2C  
Support for STMicroelectronics ST33ZP24 I2C devices.  
Requires TPM\\_ST33ZP24 and I2C.

CONFIG\\_TPM\\_ST33ZP24\\_SPI  
Support for STMicroelectronics ST33ZP24 SPI devices.  
Requires TPM\\_ST33ZP24 and SPI.

CONFIG\\_TPM\\_ATMEL\\_TWI  
Support for Atmel TWI TPM device. Requires I2C support.

CONFIG\\_TPM\\_TIS\\_LPC  
Support for generic parallel port TPM devices. Only one device  
per system is supported at this time.

CONFIG\\_TPM\\_TIS\\_BASE\\_ADDRESS  
Base address where the generic TPM device is mapped  
to. Contemporary x86 systems usually map it at  
0xfed40000.

CONFIG\\_TPM  
Define this to enable the TPM support library which provides  
functional interfaces to some TPM commands.  
Requires support for a TPM device.

CONFIG\\_TPM\\_AUTH\\_SESSIONS



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Define this to enable authorized functions in the TPM library.  
Requires CONFIG\\_TPM and CONFIG\\_SHA1.

### - USB Support:

At the moment only the UHCI host controller is supported (PIP405, MIP405); define CONFIG\\_USB\\_UHCI to enable it. define CONFIG\\_USB\\_KEYBOARD to enable the USB Keyboard and define CONFIG\\_USB\\_STORAGE to enable the USB storage devices.

### Note:

Supported are USB Keyboards and USB Floppy drives (TEAC FD-05PUB).

CONFIG\\_USB\\_EHCI\\_TXFIFO\\_THRESH enables setting of the txfilltuning field in the EHCI controller on reset.

CONFIG\\_USB\\_DWC2\\_REG\\_ADDR the physical CPU address of the DWC2 HW module registers.

### - USB Device:

Define the below if you wish to use the USB console. Once firmware is rebuilt from a serial console issue the command `''setenv stdin usbtty; setenv stdout usbtty''` and attach your USB cable. The Unix command `''dmesg''` should print it has found a new device. The environment variable usbtty can be set to gserial or cdc\\_acm to enable your device to appear to a USB host as a Linux gserial device or a Common Device Class Abstract Control Model serial device. If you select usbtty = gserial you should be able to enumerate a Linux host by

```
\# modprobe usbserial vendor=0xVendorID product=0xProductID  
else if using cdc\_acm, simply setting the environment  
variable usbtty to be cdc\_acm should suffice. The following  
might be defined in YourBoardName.h
```

### CONFIG\\_USB\\_DEVICE

Define this to build a UDC device

### CONFIG\\_USB\\_TTY

Define this to have a tty type of device available to talk to the UDC device

### CONFIG\\_USB\\_HS

Define this to enable the high speed support for usb device and usbtty. If this feature is enabled, a routine `int is\_usbd\_high\_speed(void)` also needs to be defined by the driver to dynamically poll whether the enumeration has succeeded at high speed or full speed.

### CONFIG\\_SYS\\_CONSOLE\\_IS\\_IN\\_ENV

Define this if you want stdin, stdout &/or stderr to be set to usbtty.

If you have a USB-IF assigned VendorID then you may wish to define your own vendor specific values either in BoardName.h or directly in `usbd\_vendor\_info.h`. If you don't define CONFIG\\_USB\\_MANUFACTURER, CONFIG\\_USB\\_PRODUCT\\_NAME, CONFIG\\_USB\\_VENDORID and CONFIG\\_USB\\_PRODUCTID, then U-Boot should pretend to be a Linux device to it's target host.

### CONFIG\\_USB\\_MANUFACTURER

Define this string as the name of your company for  
- CONFIG\\_USB\\_MANUFACTURER `''my company''`

### CONFIG\\_USB\\_PRODUCT\\_NAME

Define this string as the name of your product  
- CONFIG\\_USB\\_PRODUCT\\_NAME `''acme usb device''`

### CONFIG\\_USB\\_VENDORID

Define this as your assigned Vendor ID from the USB Implementors Forum. This *\*must\** be a genuine Vendor ID to avoid polluting the USB namespace.  
- CONFIG\\_USB\\_VENDORID `0xFFFF`

### CONFIG\\_USB\\_PRODUCTID



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Define this as the unique Product ID  
for your device

- CONFIG\\_USBD\\_PRODUCTID 0xFFFF

- ULPI Layer Support:

The ULPI (UTMI Low Pin (count) Interface) PHYs are supported via the generic ULPI layer. The generic layer accesses the ULPI PHY via the platform viewport, so you need both the generic layer and the viewport enabled. Currently only Chipidea/ARC based viewport is supported.

To enable the ULPI layer support, define CONFIG\\_USB\\_ULPI and CONFIG\\_USB\\_ULPI\\_VIEWPORT in your board configuration file. If your ULPI phy needs a different reference clock than the standard 24 MHz then you have to define CONFIG\\_ULPI\\_REF\\_CLK to the appropriate value in Hz.

- MMC Support:

The MMC controller on the Intel PXA is supported. To enable this define CONFIG\\_MMC. The MMC can be accessed from the boot prompt by mapping the device to physical memory similar to flash. Command line is enabled with CONFIG\\_CMD\\_MMC. The MMC driver also works with the FAT fs. This is enabled with CONFIG\\_CMD\\_FAT.

CONFIG\\_SH\\_MMCIF

Support for Renesas on-chip MMCIF controller

CONFIG\\_SH\\_MMCIF\\_ADDR

Define the base address of MMCIF registers

CONFIG\\_SH\\_MMCIF\\_CLK

Define the clock frequency for MMCIF

- USB Device Firmware Update (DFU) class support:

CONFIG\\_DFU\\_OVER\\_USB

This enables the USB portion of the DFU USB class

CONFIG\\_DFU\\_NAND

This enables support for exposing NAND devices via DFU.

CONFIG\\_DFU\\_RAM

This enables support for exposing RAM via DFU.

Note: DFU spec refer to non-volatile memory usage, but allow usages beyond the scope of spec - here RAM usage, one that would help mostly the developer.

CONFIG\\_SYS\\_DFU\\_DATA\\_BUF\\_SIZE

Dfu transfer uses a buffer before writing data to the raw storage device. Make the size (in bytes) of this buffer configurable. The size of this buffer is also configurable through the 'dfu\\_bufsiz' environment variable.

CONFIG\\_SYS\\_DFU\\_MAX\\_FILE\\_SIZE

When updating files rather than the raw storage device, we use a static buffer to copy the file into and then write the buffer once we've been given the whole file. Define this to the maximum filesize (in bytes) for the buffer. Default is 4 MiB if undefined.

DFU\\_DEFAULT\\_POLL\\_TIMEOUT

Poll timeout {\lbrack}ms{\rbrack}, is the timeout a device can send to the host. The host must wait for this timeout before sending a subsequent DFU\\_GET\\_STATUS request to the device.

DFU\\_MANIFEST\\_POLL\\_TIMEOUT

Poll timeout {\lbrack}ms{\rbrack}, which the device sends to the host when entering dfuMANIFEST state. Host waits this timeout, before sending again an USB request to the device.

- Journaling Flash filesystem support:

CONFIG\\_JFFS2\\_NAND

Define these for a default partition on a NAND device

CONFIG\\_SYS\\_JFFS2\\_FIRST\\_SECTOR,

CONFIG\\_SYS\\_JFFS2\\_FIRST\\_BANK, CONFIG\\_SYS\\_JFFS2\\_NUM\\_BANKS

Define these for a default partition on a NOR device



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- Keyboard Support:  
See Kconfig help for available keyboard drivers.

CONFIG\\_KEYBOARD

Define this to enable a custom keyboard support.  
This simply calls `drv\_keyboard\_init()` which must be defined in your board-specific files. This option is deprecated and is only used by novena. For new boards, use driver model instead.

- Video support:  
CONFIG\\_FSL\\_DIU\\_FB  
Enable the Freescale DIU video driver. Reference boards for SOCs that have a DIU should define this macro to enable DIU support, and should also define these other macros:

CONFIG\\_SYS\\_DIU\\_ADDR  
CONFIG\\_VIDEO  
CONFIG\\_CFB\\_CONSOLE  
CONFIG\\_VIDEO\\_SW\\_CURSOR  
CONFIG\\_VGA\\_AS\\_SINGLE\\_DEVICE  
CONFIG\\_VIDEO\\_LOGO  
CONFIG\\_VIDEO\\_BMP\\_LOGO

The DIU driver will look for the 'video-mode' environment variable, and if defined, enable the DIU as a console during boot. See the documentation file `doc/README.video` for a description of this variable.

- LCD Support: CONFIG\\_LCD

Define this to enable LCD support (for output to LCD display); also select one of the supported displays by defining one of these:

CONFIG\\_ATMEL\\_LCD:

HITACHI TX09D70VM1CCA, 3.5'', 240x320.

CONFIG\\_NEC\\_NL6448AC33:

NEC NL6448AC33-18. Active, color, single scan.

CONFIG\\_NEC\\_NL6448BC20

NEC NL6448BC20-08. 6.5'', 640x480.  
Active, color, single scan.

CONFIG\\_NEC\\_NL6448BC33\\_54

NEC NL6448BC33-54. 10.4'', 640x480.  
Active, color, single scan.

CONFIG\\_SHARP\\_16x9

Sharp 320x240. Active, color, single scan.  
It isn't 16x9, and I am not sure what it is.

CONFIG\\_SHARP\\_LQ64D341

Sharp LQ64D341 display, 640x480.  
Active, color, single scan.

CONFIG\\_HLD1045

HLD1045 display, 640x480.  
Active, color, single scan.

CONFIG\\_OPTREX\\_BW

Optrex CBL50840-2 NF-FW 99 22 M5  
or  
Hitachi LMG6912RPF00T  
or  
Hitachi SP14Q002



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320x240. Black \& white.

### CONFIG\\_LCD\\_ALIGNMENT

Normally the LCD is page-aligned (typically 4KB). If this is defined then the LCD will be aligned to this value instead. For ARM it is sometimes useful to use MMU\\_SECTION\\_SIZE here, since it is cheaper to change data cache settings on a per-section basis.

### CONFIG\\_LCD\\_ROTATION

Sometimes, for example if the display is mounted in portrait mode or even if it's mounted landscape but rotated by 180degree, we need to rotate our content of the display relative to the framebuffer, so that user can read the messages which are printed out.

Once CONFIG\\_LCD\\_ROTATION is defined, the lcd\\_console will be initialized with a given rotation from ''vl\\_rot'' out of ''vidinfo\\_t'' which is provided by the board specific code. The value for vl\\_rot is coded as following (matching to fbcon=rotate: linux-kernel commandline):

0 = no rotation respectively 0 degree  
1 = 90 degree rotation  
2 = 180 degree rotation  
3 = 270 degree rotation

If CONFIG\\_LCD\\_ROTATION is not defined, the console will be initialized with 0degree rotation.

### CONFIG\\_LCD\\_BMP\\_RLE8

Support drawing of RLE8-compressed bitmaps on the LCD.

### CONFIG\\_I2C\\_EDID

Enables an 'i2c edid' command which can read EDID information over I2C from an attached LCD display.

- MII/PHY support:

CONFIG\\_PHY\\_CLOCK\\_FREQ (ppc4xx)

The clock frequency of the MII bus

### CONFIG\\_PHY\\_RESET\\_DELAY

Some PHY like Intel LXT971A need extra delay after reset before any MII register access is possible. For such PHY, set this option to the usec delay required. (minimum 300usec for LXT971A)

CONFIG\\_PHY\\_CMD\\_DELAY (ppc4xx)

Some PHY like Intel LXT971A need extra delay after command issued before MII status register can be read

- IP address:

CONFIG\\_IPADDR

Define a default value for the IP address to use for the default Ethernet interface, in case this is not determined through e.g. bootp.  
(Environment variable ''ipaddr'')

- Server IP address:

CONFIG\\_SERVERIP

Defines a default value for the IP address of a TFTP server to contact when using the ''tftboot'' command.  
(Environment variable ''serverip'')

CONFIG\\_KEEP\\_SERVERADDR

Keeps the server's MAC address, in the env 'serveraddr' for passing to bootargs (like Linux's netconsole option)

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- Gateway IP address:  
CONFIG\\_GATEWAYIP

Defines a default value for the IP address of the default router where packets to other networks are sent to.  
(Environment variable 'gatewayip')

- Subnet mask:  
CONFIG\\_NETMASK

Defines a default value for the subnet mask (or routing prefix) which is used to determine if an IP address belongs to the local subnet or needs to be forwarded through a router.  
(Environment variable 'netmask')

- BOOTP Recovery Mode:  
CONFIG\\_BOOTP\\_RANDOM\\_DELAY

If you have many targets in a network that try to boot using BOOTP, you may want to avoid that all systems send out BOOTP requests at precisely the same moment (which would happen for instance at recovery from a power failure, when all systems will try to boot, thus flooding the BOOTP server. Defining CONFIG\\_BOOTP\\_RANDOM\\_DELAY causes a random delay to be inserted before sending out BOOTP requests. The following delays are inserted then:

1st BOOTP request: delay 0 ... 1 sec  
2nd BOOTP request: delay 0 ... 2 sec  
3rd BOOTP request: delay 0 ... 4 sec  
4th and following  
BOOTP requests: delay 0 ... 8 sec

CONFIG\\_BOOTP\\_ID\\_CACHE\\_SIZE

BOOTP packets are uniquely identified using a 32-bit ID. The server will copy the ID from client requests to responses and U-Boot will use this to determine if it is the destination of an incoming response. Some servers will check that addresses aren't in use before handing them out (usually using an ARP ping) and therefore take up to a few hundred milliseconds to respond. Network congestion may also influence the time it takes for a response to make it back to the client. If that time is too long, U-Boot will retransmit requests. In order to allow earlier responses to still be accepted after these retransmissions, U-Boot's BOOTP client keeps a small cache of IDs. The CONFIG\\_BOOTP\\_ID\\_CACHE\\_SIZE controls the size of this cache. The default is to keep IDs for up to four outstanding requests. Increasing this will allow U-Boot to accept offers from a BOOTP client in networks with unusually high latency.

- DHCP Advanced Options:  
You can fine tune the DHCP functionality by defining CONFIG\\_BOOTP\\_\*\_ symbols:

CONFIG\\_BOOTP\\_NISDOMAIN  
CONFIG\\_BOOTP\\_BOOTFILESIZE  
CONFIG\\_BOOTP\\_NTPSERVER  
CONFIG\\_BOOTP\\_TIMEOFFSET  
CONFIG\\_BOOTP\\_VENDOREX  
CONFIG\\_BOOTP\\_MAY\\_FAIL

CONFIG\\_BOOTP\\_SERVERIP - TFTP server will be the serverip environment variable, not the BOOTP server.

CONFIG\\_BOOTP\\_MAY\\_FAIL - If the DHCP server is not found after the configured retry count, the call will fail instead of starting over. This can be used to fail over to Link-local IP address configuration if the DHCP server is not available.

CONFIG\\_BOOTP\\_DHCP\\_REQUEST\\_DELAY

A 32bit value in microseconds for a delay between



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receiving a ''DHCP Offer'' and sending the ''DHCP Request''. This fixes a problem with certain DHCP servers that don't respond 100% of the time to a ''DHCP request''. E.g. On an AT91RM9200 processor running at 180MHz, this delay needed to be \*at least\* 15,000 usec before a Windows Server 2003 DHCP server would reply 100% of the time. I recommend at least 50,000 usec to be safe. The alternative is to hope that one of the retries will be successful but note that the DHCP timeout and retry process takes a longer than this delay.

- Link-local IP address negotiation:  
Negotiate with other link-local clients on the local network for an address that doesn't require explicit configuration. This is especially useful if a DHCP server cannot be guaranteed to exist in all environments that the device must operate.

See doc/README.link-local for more information.

- MAC address from environment variables

FDT\\_SEQ\\_MACADDR\\_FROM\\_ENV

Fix-up device tree with MAC addresses fetched sequentially from environment variables. This config work on assumption that non-usable ethernet node of device-tree are either not present or their status has been marked as ''disabled''.

- CDP Options:  
CONFIG\\_CDP\\_DEVICE\\_ID

The device id used in CDP trigger frames.

CONFIG\\_CDP\\_DEVICE\\_ID\\_PREFIX

A two character string which is prefixed to the MAC address of the device.

CONFIG\\_CDP\\_PORT\\_ID

A printf format string which contains the ascii name of the port. Normally is set to ''eth%d'' which sets eth0 for the first Ethernet, eth1 for the second etc.

CONFIG\\_CDP\\_CAPABILITIES

A 32bit integer which indicates the device capabilities; 0x00000010 for a normal host which does not forwards.

CONFIG\\_CDP\\_VERSION

An ascii string containing the version of the software.

CONFIG\\_CDP\\_PLATFORM

An ascii string containing the name of the platform.

CONFIG\\_CDP\\_TRIGGER

A 32bit integer sent on the trigger.

CONFIG\\_CDP\\_POWER\\_CONSUMPTION

A 16bit integer containing the power consumption of the device in .1 of milliwatts.

CONFIG\\_CDP\\_APPLIANCE\\_VLAN\\_TYPE

A byte containing the id of the VLAN.

- Status LED: CONFIG\\_LED\\_STATUS

Several configurations allow to display the current status using a LED. For instance, the LED will blink fast while running U-Boot code, stop blinking as soon as a reply to a BOOTP request was received, and start blinking slow once the Linux kernel is running





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(supported by a status LED driver in the Linux kernel). Defining CONFIG\\_LED\\_STATUS enables this feature in U-Boot.

Additional options:

### CONFIG\\_LED\\_STATUS\\_GPIO

The status LED can be connected to a GPIO pin. In such cases, the gpio\\_led driver can be used as a status LED backend implementation. Define CONFIG\\_LED\\_STATUS\\_GPIO to include the gpio\\_led driver in the U-Boot binary.

### CONFIG\\_GPIO\\_LED\\_INVERTED\\_TABLE

Some GPIO connected LEDs may have inverted polarity in which case the GPIO high value corresponds to LED off state and GPIO low value corresponds to LED on state. In such cases CONFIG\\_GPIO\\_LED\\_INVERTED\\_TABLE may be defined with a list of GPIO LEDs that have inverted polarity.

### - I2C Support: CONFIG\\_SYS\\_I2C

This enable the NEW i2c subsystem, and will allow you to use i2c commands at the u-boot command line (as long as you set CONFIG\\_SYS\\_I2C\\_SOFT\\_SPEED and CONFIG\\_SYS\\_I2C\\_SOFT\\_SLAVE

for defining speed and slave address  
- activate second bus with I2C\\_SOFT\\_DECLARATIONS2 define CONFIG\\_SYS\\_I2C\\_SOFT\\_SPEED\\_2 and CONFIG\\_SYS\\_I2C\\_SOFT\\_SLAVE\\_2 for defining speed and slave address  
- activate third bus with I2C\\_SOFT\\_DECLARATIONS3 define CONFIG\\_SYS\\_I2C\\_SOFT\\_SPEED\\_3 and CONFIG\\_SYS\\_I2C\\_SOFT\\_SLAVE\\_3 for defining speed and slave address  
- activate fourth bus with I2C\\_SOFT\\_DECLARATIONS4 define CONFIG\\_SYS\\_I2C\\_SOFT\\_SPEED\\_4 and CONFIG\\_SYS\\_I2C\\_SOFT\\_SLAVE\\_4 for defining speed and slave address

- drivers/i2c/fsl\\_i2c.c:  
- activate i2c driver with CONFIG\\_SYS\\_I2C\\_FSL  
define CONFIG\\_SYS\\_FSL\\_I2C\\_OFFSET for setting the register offset CONFIG\\_SYS\\_FSL\\_I2C\\_SPEED for the i2c speed and CONFIG\\_SYS\\_FSL\\_I2C\\_SLAVE for the slave addr of the first bus.

- If your board supports a second fsl i2c bus, define CONFIG\\_SYS\\_FSL\\_I2C2\\_OFFSET for the register offset CONFIG\\_SYS\\_FSL\\_I2C2\\_SPEED for the speed and CONFIG\\_SYS\\_FSL\\_I2C2\\_SLAVE for the slave address of the second bus.

- drivers/i2c/tegra\\_i2c.c:  
- activate this driver with CONFIG\\_SYS\\_I2C\\_TEGRA  
- This driver adds 4 i2c buses with a fix speed from 100000 and the slave addr 0!

- drivers/i2c/ppc4xx\\_i2c.c  
- activate this driver with CONFIG\\_SYS\\_I2C\\_PPC4XX  
- CONFIG\\_SYS\\_I2C\\_PPC4XX\\_CHO activate hardware channel 0  
- CONFIG\\_SYS\\_I2C\\_PPC4XX\\_CH1 activate hardware channel 1

- drivers/i2c/i2c\\_mxc.c  
- activate this driver with CONFIG\\_SYS\\_I2C\\_MXC  
- enable bus 1 with CONFIG\\_SYS\\_I2C\\_MXC\\_I2C1  
- enable bus 2 with CONFIG\\_SYS\\_I2C\\_MXC\\_I2C2  
- enable bus 3 with CONFIG\\_SYS\\_I2C\\_MXC\\_I2C3  
- enable bus 4 with CONFIG\\_SYS\\_I2C\\_MXC\\_I2C4  
- define speed for bus 1 with CONFIG\\_SYS\\_MXC\\_I2C1\\_SPEED  
- define slave for bus 1 with CONFIG\\_SYS\\_MXC\\_I2C1\\_SLAVE  
- define speed for bus 2 with CONFIG\\_SYS\\_MXC\\_I2C2\\_SPEED  
- define slave for bus 2 with CONFIG\\_SYS\\_MXC\\_I2C2\\_SLAVE  
- define speed for bus 3 with CONFIG\\_SYS\\_MXC\\_I2C3\\_SPEED  
- define slave for bus 3 with CONFIG\\_SYS\\_MXC\\_I2C3\\_SLAVE  
- define speed for bus 4 with CONFIG\\_SYS\\_MXC\\_I2C4\\_SPEED  
- define slave for bus 4 with CONFIG\\_SYS\\_MXC\\_I2C4\\_SLAVE  
If those defines are not set, default value is 100000 for speed, and 0 for slave.

- drivers/i2c/rcar\\_i2c.c:  
- activate this driver with CONFIG\\_SYS\\_I2C\\_RCAR  
- This driver adds 4 i2c buses



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```
- drivers/i2c/sh\_i2c.c:  
- activate this driver with CONFIG\_SYS\_I2C\_SH  
- This driver adds from 2 to 5 i2c buses  
  
- CONFIG\_SYS\_I2C\_SH\_BASE0 for setting the register channel 0  
- CONFIG\_SYS\_I2C\_SH\_SPEED0 for for the speed channel 0  
- CONFIG\_SYS\_I2C\_SH\_BASE1 for setting the register channel 1  
- CONFIG\_SYS\_I2C\_SH\_SPEED1 for for the speed channel 1  
- CONFIG\_SYS\_I2C\_SH\_BASE2 for setting the register channel 2  
- CONFIG\_SYS\_I2C\_SH\_SPEED2 for for the speed channel 2  
- CONFIG\_SYS\_I2C\_SH\_BASE3 for setting the register channel 3  
- CONFIG\_SYS\_I2C\_SH\_SPEED3 for for the speed channel 3  
- CONFIG\_SYS\_I2C\_SH\_BASE4 for setting the register channel 4  
- CONFIG\_SYS\_I2C\_SH\_SPEED4 for for the speed channel 4  
- CONFIG\_SYS\_I2C\_SH\_NUM\_CONTROLLERS for number of i2c buses
```

```
- drivers/i2c/omap24xx\_i2c.c  
- activate this driver with CONFIG\_SYS\_I2C\_OMAP24XX  
- CONFIG\_SYS\_OMAP24\_I2C\_SPEED speed channel 0  
- CONFIG\_SYS\_OMAP24\_I2C\_SLAVE slave addr channel 0  
- CONFIG\_SYS\_OMAP24\_I2C\_SPEED1 speed channel 1  
- CONFIG\_SYS\_OMAP24\_I2C\_SLAVE1 slave addr channel 1  
- CONFIG\_SYS\_OMAP24\_I2C\_SPEED2 speed channel 2  
- CONFIG\_SYS\_OMAP24\_I2C\_SLAVE2 slave addr channel 2  
- CONFIG\_SYS\_OMAP24\_I2C\_SPEED3 speed channel 3  
- CONFIG\_SYS\_OMAP24\_I2C\_SLAVE3 slave addr channel 3  
- CONFIG\_SYS\_OMAP24\_I2C\_SPEED4 speed channel 4  
- CONFIG\_SYS\_OMAP24\_I2C\_SLAVE4 slave addr channel 4
```

```
- drivers/i2c/s3c24x0\_i2c.c:  
- activate this driver with CONFIG\_SYS\_I2C\_S3C24X0  
- This driver adds i2c buses (11 for Exynos5250, Exynos5420  
9 i2c buses for Exynos4 and 1 for S3C24X0 SoCs from Samsung)  
with a fix speed from 100000 and the slave addr 0!
```

```
- drivers/i2c/ihs\_i2c.c  
- activate this driver with CONFIG\_SYS\_I2C\_IHS  
- CONFIG\_SYS\_I2C\_IHS\_CH0 activate hardware channel 0  
- CONFIG\_SYS\_I2C\_IHS\_SPEED\_0 speed channel 0  
- CONFIG\_SYS\_I2C\_IHS\_SLAVE\_0 slave addr channel 0  
- CONFIG\_SYS\_I2C\_IHS\_CH1 activate hardware channel 1  
- CONFIG\_SYS\_I2C\_IHS\_SPEED\_1 speed channel 1  
- CONFIG\_SYS\_I2C\_IHS\_SLAVE\_1 slave addr channel 1  
- CONFIG\_SYS\_I2C\_IHS\_CH2 activate hardware channel 2  
- CONFIG\_SYS\_I2C\_IHS\_SPEED\_2 speed channel 2  
- CONFIG\_SYS\_I2C\_IHS\_SLAVE\_2 slave addr channel 2  
- CONFIG\_SYS\_I2C\_IHS\_CH3 activate hardware channel 3  
- CONFIG\_SYS\_I2C\_IHS\_SPEED\_3 speed channel 3  
- CONFIG\_SYS\_I2C\_IHS\_SLAVE\_3 slave addr channel 3  
- activate dual channel with CONFIG\_SYS\_I2C\_IHS\_DUAL  
- CONFIG\_SYS\_I2C\_IHS\_SPEED\_0\_1 speed channel 0\_1  
- CONFIG\_SYS\_I2C\_IHS\_SLAVE\_0\_1 slave addr channel 0\_1  
- CONFIG\_SYS\_I2C\_IHS\_SPEED\_1\_1 speed channel 1\_1  
- CONFIG\_SYS\_I2C\_IHS\_SLAVE\_1\_1 slave addr channel 1\_1  
- CONFIG\_SYS\_I2C\_IHS\_SPEED\_2\_1 speed channel 2\_1  
- CONFIG\_SYS\_I2C\_IHS\_SLAVE\_2\_1 slave addr channel 2\_1  
- CONFIG\_SYS\_I2C\_IHS\_SPEED\_3\_1 speed channel 3\_1  
- CONFIG\_SYS\_I2C\_IHS\_SLAVE\_3\_1 slave addr channel 3\_1
```

additional defines:

```
CONFIG\_SYS\_NUM\_I2C\_BUSES  
Hold the number of i2c buses you want to use.
```

```
CONFIG\_SYS\_I2C\_DIRECT\_BUS  
define this, if you don't use i2c muxes on your hardware.  
if CONFIG\_SYS\_I2C\_MAX\_HOPS is not defined or == 0 you can  
omit this define.
```

```
CONFIG\_SYS\_I2C\_MAX\_HOPS  
define how many muxes are maximal consecutively connected  
on one i2c bus. If you not use i2c muxes, omit this  
define.
```

```
CONFIG\_SYS\_I2C\_BUSES  
hold a list of buses you want to use, only used if
```

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CONFIG\\_SYS\\_I2C\\_DIRECT\\_BUS is not defined, for example  
a board with CONFIG\\_SYS\\_I2C\\_MAX\\_HOPS = 1 and  
CONFIG\\_SYS\\_NUM\\_I2C\\_BUSES = 9:

```
CONFIG\_SYS\_I2C\_BUSES \{\{0, \{I2C\_NULL\_HOP\}\}, \textbackslash\}
\{0, \{\{I2C\_MUX\_PCA9547, 0x70, 1\}\}\}, \textbackslash\}
\{0, \{\{I2C\_MUX\_PCA9547, 0x70, 2\}\}\}, \textbackslash\}
\{0, \{\{I2C\_MUX\_PCA9547, 0x70, 3\}\}\}, \textbackslash\}
\{0, \{\{I2C\_MUX\_PCA9547, 0x70, 4\}\}\}, \textbackslash\}
\{0, \{\{I2C\_MUX\_PCA9547, 0x70, 5\}\}\}, \textbackslash\}
\{1, \{I2C\_NULL\_HOP\}\}, \textbackslash\}
\{1, \{\{I2C\_MUX\_PCA9544, 0x72, 1\}\}\}, \textbackslash\}
\{1, \{\{I2C\_MUX\_PCA9544, 0x72, 2\}\}\}, \textbackslash\}
\}
```

which defines

bus 0 on adapter 0 without a mux  
bus 1 on adapter 0 with a PCA9547 on address 0x70 port 1  
bus 2 on adapter 0 with a PCA9547 on address 0x70 port 2  
bus 3 on adapter 0 with a PCA9547 on address 0x70 port 3  
bus 4 on adapter 0 with a PCA9547 on address 0x70 port 4  
bus 5 on adapter 0 with a PCA9547 on address 0x70 port 5  
bus 6 on adapter 1 without a mux  
bus 7 on adapter 1 with a PCA9544 on address 0x72 port 1  
bus 8 on adapter 1 with a PCA9544 on address 0x72 port 2

If you do not have i2c muxes on your board, omit this define.

- Legacy I2C Support:

If you use the software i2c interface (CONFIG\\_SYS\\_I2C\\_SOFT)  
then the following macros need to be defined (examples are  
from include/configs/lwmon.h):

I2C\\_INIT

(Optional). Any commands necessary to enable the I2C  
controller or configure ports.

eg: `\#define I2C\_INIT (immr->im\_cpm.cp\_pbdir |= PB\_SCL)`

I2C\\_ACTIVE

The code necessary to make the I2C data line active  
(driven). If the data line is open collector, this  
define can be null.

eg: `\#define I2C\_ACTIVE (immr->im\_cpm.cp\_pbdir |= PB\_SDA)`

I2C\\_TRISTATE

The code necessary to make the I2C data line tri-stated  
(inactive). If the data line is open collector, this  
define can be null.

eg: `\#define I2C\_TRISTATE (immr->im\_cpm.cp\_pbdir \&= \textasciitilde{PB\_SDA})`

I2C\\_READ

Code that returns true if the I2C data line is high,  
false if it is low.

eg: `\#define I2C\_READ ((immr->im\_cpm.cp\_pbdat \& PB\_SDA) != 0)`

I2C\\_SDA(bit)

If `is` is true, sets the I2C data line high. If it  
is false, it clears it (low).

eg: `\#define I2C\_SDA(bit) \textbackslash\}`  
`if(bit) immr->im\_cpm.cp\_pbdat |= PB\_SDA; \textbackslash\}`  
`else immr->im\_cpm.cp\_pbdat \&= \textasciitilde{PB\_SDA}`

I2C\\_SCL(bit)

If `is` is true, sets the I2C clock line high. If it  
is false, it clears it (low).



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```
eg: \#define I2C\_SCL(bit) \textbackslash{}
if(bit) immr->im\_cpm.cp\_pbdatt |= PB\_SCL; \textbackslash{}
else immr->im\_cpm.cp\_pbdatt \&= \textasciitilde{}PB\_SCL
```

I2C\\_DELAY

This delay is invoked four times per clock cycle so this controls the rate of data transfer. The data rate thus is  $1 / (I2C\_DELAY * 4)$ . Often defined to be something like:

```
\#define I2C\_DELAY udelay(2)
```

```
CONFIG\_SOFT\_I2C\_GPIO\_SCL / CONFIG\_SOFT\_I2C\_GPIO\_SDA
```

If your arch supports the generic GPIO framework (asm/gpio.h), then you may alternatively define the two GPIOs that are to be used as SCL / SDA. Any of the previous I2C\\_xxx macros will have GPIO-based defaults assigned to them as appropriate.

You should define these to the GPIO value as given directly to the generic GPIO functions.

```
CONFIG\_SYS\_I2C\_INIT\_BOARD
```

When a board is reset during an i2c bus transfer chips might think that the current transfer is still in progress. On some boards it is possible to access the i2c SCLK line directly, either by using the processor pin as a GPIO or by having a second pin connected to the bus. If this option is defined a custom i2c\\_init\\_board() routine in boards/xxx/board.c is run early in the boot sequence.

```
CONFIG\_I2C\_MULTI\_BUS
```

This option allows the use of multiple I2C buses, each of which must have a controller. At any point in time, only one bus is active. To switch to a different bus, use the 'i2c dev' command. Note that bus numbering is zero-based.

```
CONFIG\_SYS\_I2C\_NOPROBES
```

This option specifies a list of I2C devices that will be skipped when the 'i2c probe' command is issued. If CONFIG\\_I2C\\_MULTI\\_BUS is set, specify a list of bus-device pairs. Otherwise, specify a 1D array of device addresses

```
e.g.
\#undef CONFIG\_I2C\_MULTI\_BUS
\#define CONFIG\_SYS\_I2C\_NOPROBES \{0x50,0x68\}
```

will skip addresses 0x50 and 0x68 on a board with one I2C bus

```
\#define CONFIG\_I2C\_MULTI\_BUS
\#define CONFIG\_SYS\_I2C\_NOPROBES \{\{0,0x50\},\{0,0x68\},\{1,0x54\}\}
```

will skip addresses 0x50 and 0x68 on bus 0 and address 0x54 on bus 1

```
CONFIG\_SYS\_SPD\_BUS\_NUM
```

If defined, then this indicates the I2C bus number for DDR SPD. If not defined, then U-Boot assumes that SPD is on I2C bus 0.

```
CONFIG\_SYS\_RTC\_BUS\_NUM
```

If defined, then this indicates the I2C bus number for the RTC. If not defined, then U-Boot assumes that RTC is on I2C bus 0.

```
CONFIG\_SOFT\_I2C\_READ\_REPEATED\_START
```

defining this will force the i2c\\_read() function in the soft\\_i2c driver to perform an I2C repeated start between writing the address pointer and reading the data. If this define is omitted the default behaviour of doing a stop-start sequence will be used. Most I2C devices can use either method, but some require one or



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the other.

- SPI Support: CONFIG\\_SPI

Enables SPI driver (so far only tested with SPI EEPROM, also an instance works with Crystal A/D and D/As on the SACSng board)

CONFIG\\_SOFT\\_SPI

Enables a software (bit-bang) SPI driver rather than using hardware support. This is a general purpose driver that only requires three general I/O port pins (two outputs, one input) to function. If this is defined, the board configuration must define several SPI configuration items (port pins to use, etc). For an example, see include/configs/sacsng.h.

CONFIG\\_SYS\\_SPI\\_MXC\\_WAIT

Timeout for waiting until spi transfer completed.  
default: (CONFIG\\_SYS\\_HZ/100) /\* 10 ms \*/

- FPGA Support: CONFIG\\_FPGA

Enables FPGA subsystem.

CONFIG\\_FPGA\\_

Enables support for specific chip vendors.  
(ALTERA, XILINX)

CONFIG\\_FPGA\\_

Enables support for FPGA family.  
(SPARTAN2, SPARTAN3, VIRTEX2, CYCLONE2, ACEX1K, ACEX)

CONFIG\\_FPGA\\_COUNT

Specify the number of FPGA devices to support.

CONFIG\\_SYS\\_FPGA\\_PROG\\_FEEDBACK

Enable printing of hash marks during FPGA configuration.

CONFIG\\_SYS\\_FPGA\\_CHECK\\_BUSY

Enable checks on FPGA configuration interface busy status by the configuration function. This option will require a board or device specific function to be written.

CONFIG\\_FPGA\\_DELAY

If defined, a function that provides delays in the FPGA configuration driver.

CONFIG\\_SYS\\_FPGA\\_CHECK\\_CTRLC

Allow Control-C to interrupt FPGA configuration

CONFIG\\_SYS\\_FPGA\\_CHECK\\_ERROR

Check for configuration errors during FPGA bitfile loading. For example, abort during Virtex II configuration if the INIT\\_B line goes low (which indicated a CRC error).

CONFIG\\_SYS\\_FPGA\\_WAIT\\_INIT

Maximum time to wait for the INIT\\_B line to de-assert after PROB\\_B has been de-asserted during a Virtex II FPGA configuration sequence. The default time is 500 ms.

CONFIG\\_SYS\\_FPGA\\_WAIT\\_BUSY

Maximum time to wait for BUSY to de-assert during Virtex II FPGA configuration. The default is 5 ms.

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CONFIG\\_SYS\\_FPGA\\_WAIT\\_CONFIG

Time to wait after FPGA configuration. The default is 200 ms.

- Configuration Management:

CONFIG\\_IDENT\\_STRING

If defined, this string will be added to the U-Boot version information (U\\_BOOT\\_VERSION)

- Vendor Parameter Protection:

U-Boot considers the values of the environment variables ''serial\#' (Board Serial Number) and ''ethaddr'' (Ethernet Address) to be parameters that are set once by the board vendor / manufacturer, and protects these variables from casual modification by the user. Once set, these variables are read-only, and write or delete attempts are rejected. You can change this behaviour:

If CONFIG\\_ENV\\_OVERWRITE is \#defined in your config file, the write protection for vendor parameters is completely disabled. Anybody can change or delete these parameters.

Alternatively, if you define \\_both\\_ an ethaddr in the default env \\_and\\_ CONFIG\\_OVERWRITE\\_ETHADDR\\_ONCE, a default Ethernet address is installed in the environment, which can be changed exactly ONCE by the user. {\lbrack}The serial\# is unaffected by this, i. e. it remains read-only.{\rbrack}

The same can be accomplished in a more flexible way for any variable by configuring the type of access to allow for those variables in the ''flags'' variable or define CONFIG\\_ENV\\_FLAGS\\_LIST\\_STATIC.

- Protected RAM:

CONFIG\\_PRAM

Define this variable to enable the reservation of ''protected RAM'', i. e. RAM which is not overwritten by U-Boot. Define CONFIG\\_PRAM to hold the number of kB you want to reserve for pRAM. You can overwrite this default value by defining an environment variable ''pram'' to the number of kB you want to reserve. Note that the board info structure will still show the full amount of RAM. If pRAM is reserved, a new environment variable ''mem'' will automatically be defined to hold the amount of remaining RAM in a form that can be passed as boot argument to Linux, for instance like that:

```
setenv bootargs ... mem=\textbackslash{\}\${mem}\}  
saveenv
```

This way you can tell Linux not to use this memory, either, which results in a memory region that will not be affected by reboots.

\*WARNING\* If your board configuration uses automatic detection of the RAM size, you must make sure that this memory test is non-destructive. So far, the following board configurations are known to be ''pRAM-clean'':

IVMS8, IVML24, SPD8xx,  
HERMES, IP860, RPXlite, LWMON,  
FLAGADM

- Access to physical memory region (> 4GB)

Some basic support is provided for operations on memory not normally accessible to U-Boot - e.g. some architectures



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support access to more than 4GB of memory on 32-bit machines using physical address extension or similar. Define CONFIG\\_PHYSMEM to access this basic support, which currently only supports clearing the memory.

- Error Recovery:  
CONFIG\\_NET\\_RETRY\\_COUNT

This variable defines the number of retries for network operations like ARP, RARP, TFTP, or BOOTP before giving up the operation. If not defined, a default value of 5 is used.

CONFIG\\_ARP\\_TIMEOUT

Timeout waiting for an ARP reply in milliseconds.

CONFIG\\_NFS\\_TIMEOUT

Timeout in milliseconds used in NFS protocol. If you encounter 'ERROR: Cannot umount' in nfs command, try longer timeout such as  
\#define CONFIG\\_NFS\\_TIMEOUT 10000UL

Note:

In the current implementation, the local variables space and global environment variables space are separated. Local variables are those you define by simply typing 'name=value'. To access a local variable later on, you have write '\\$name' or '\\${name}'; to execute the contents of a variable directly type '\\$name' at the command prompt.

Global environment variables are those you use setenv/printenv to work with. To run a command stored in such a variable, you need to use the run command, and you must not use the '\\$' sign to access them.

To store commands and special characters in a variable, please use double quotation marks surrounding the whole text of the variable, instead of the backslashes before semicolons and special symbols.

- Command Line Editing and History:  
CONFIG\\_CMDLINE\\_PS\\_SUPPORT

Enable support for changing the command prompt string at run-time. Only static string is supported so far. The string is obtained from environment variables PS1 and PS2.

- Default Environment:  
CONFIG\\_EXTRA\\_ENV\\_SETTINGS

Define this to contain any number of null terminated strings (variable = value pairs) that will be part of the default environment compiled into the boot image.

For example, place something like this in your board's config file:

```
\#define CONFIG\_EXTRA\_ENV\_SETTINGS \textbackslash{}  
'myvar1=value1\textbackslash{}0' \textbackslash{}  
'myvar2=value2\textbackslash{}0'
```

Warning: This method is based on knowledge about the internal format how the environment is stored by the U-Boot code. This is NOT an official, exported interface! Although it is unlikely that this format will change soon, there is no guarantee either. You better know what you are doing here.

Note: overly (ab)use of the default environment is discouraged. Make sure to check other ways to preset the environment like the 'source' command or the



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boot command first.

CONFIG\\_DELAY\\_ENVIRONMENT

Normally the environment is loaded when the board is initialised so that it is available to U-Boot. This inhibits that so that the environment is not available until explicitly loaded later by U-Boot code. With CONFIG\\_OF\\_CONTROL this is instead controlled by the value of /config/load-environment.

- TFTP Fixed UDP Port:  
CONFIG\\_TFTP\\_PORT

If this is defined, the environment variable tftpsrcp is used to supply the TFTP UDP source port value. If tftpsrcp isn't defined, the normal pseudo-random port number generator is used.

Also, the environment variable tftpdstp is used to supply the TFTP UDP destination port value. If tftpdstp isn't defined, the normal port 69 is used.

The purpose for tftpsrcp is to allow a TFTP server to blindly start the TFTP transfer using the pre-configured target IP address and UDP port. This has the effect of 'punching through' the (Windows XP) firewall, allowing the remainder of the TFTP transfer to proceed normally. A better solution is to properly configure the firewall, but sometimes that is not allowed.

CONFIG\\_STANDALONE\\_LOAD\\_ADDR

This option defines a board specific value for the address where standalone program gets loaded, thus overwriting the architecture dependent default settings.

- Frame Buffer Address:  
CONFIG\\_FB\\_ADDR

Define CONFIG\\_FB\\_ADDR if you want to use specific address for frame buffer. This is typically the case when using a graphics controller has separate video memory. U-Boot will then place the frame buffer at the given address instead of dynamically reserving it in system RAM by calling lcd\\_setmem(), which grabs the memory for the frame buffer depending on the configured panel size.

Please see board\\_init\\_f function.

- Automatic software updates via TFTP server  
CONFIG\\_UPDATE\\_TFTP  
CONFIG\\_UPDATE\\_TFTP\\_CNT\\_MAX  
CONFIG\\_UPDATE\\_TFTP\\_MSEC\\_MAX

These options enable and control the auto-update feature; for a more detailed description refer to doc/README.update.

- MTD Support (mtdparts command, UBI support)  
CONFIG\\_MTD\\_UBI\\_WL\\_THRESHOLD

This parameter defines the maximum difference between the highest erase counter value and the lowest erase counter value of eraseblocks of UBI devices. When this threshold is exceeded, UBI starts performing wear leveling by means of moving data from eraseblock with low erase counter to eraseblocks with high erase counter.

The default value should be OK for SLC NAND flashes, NOR flashes and other flashes which have eraseblock life-cycle 100000 or more. However, in case of MLC NAND flashes which typically have eraseblock life-cycle less than 10000, the threshold should be lessened (e.g., to 128 or 256, although it does not have to be power of 2).

default: 4096

CONFIG\\_MTD\\_UBI\\_BEB\\_LIMIT





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This option specifies the maximum bad physical eraseblocks UBI expects on the MTD device (per 1024 eraseblocks). If the underlying flash does not admit of bad eraseblocks (e.g. NOR flash), this value is ignored.

NAND datasheets often specify the minimum and maximum NVM (Number of Valid Blocks) for the flashes' endurance lifetime. The maximum expected bad eraseblocks per 1024 eraseblocks then can be calculated as  $1024 * (1 - \text{MinNVB} / \text{MaxNVB})$ , which gives 20 for most NANDs (MaxNVB is basically the total count of eraseblocks on the chip).

To put it differently, if this value is 20, UBI will try to reserve about 1.9% of physical eraseblocks for bad blocks handling. And that will be 1.9% of eraseblocks on the entire NAND chip, not just the MTD partition UBI attaches. This means that if you have, say, a NAND flash chip admits maximum 40 bad eraseblocks, and it is split on two MTD partitions of the same size, UBI will reserve 40 eraseblocks when attaching a partition.

default: 20

### CONFIG\\_MTD\\_UBI\\_FASTMAP

Fastmap is a mechanism which allows attaching an UBI device in nearly constant time. Instead of scanning the whole MTD device it only has to locate a checkpoint (called fastmap) on the device. The on-flash fastmap contains all information needed to attach the device. Using fastmap makes only sense on large devices where attaching by scanning takes long. UBI will not automatically install a fastmap on old images, but you can set the UBI parameter CONFIG\\_MTD\\_UBI\\_FASTMAP\\_AUTOCONVERT to 1 if you want so. Please note that fastmap-enabled images are still usable with UBI implementations without fastmap support. On typical flash devices the whole fastmap fits into one PEB. UBI will reserve PEBs to hold two fastmaps.

### CONFIG\\_MTD\\_UBI\\_FASTMAP\\_AUTOCONVERT

Set this parameter to enable fastmap automatically on images without a fastmap.

default: 0

### CONFIG\\_MTD\\_UBI\\_FM\\_DEBUG

Enable UBI fastmap debug

default: 0

- SPL framework

### CONFIG\\_SPL

Enable building of SPL globally.

### CONFIG\\_SPL\\_LDSCRIPT

LDSCRIPT for linking the SPL binary.

### CONFIG\\_SPL\\_MAX\\_FOOTPRINT

Maximum size in memory allocated to the SPL, BSS included.

When defined, the linker checks that the actual memory used by SPL from `\_start` to `\_bss\_end` does not exceed it.

CONFIG\\_SPL\\_MAX\\_FOOTPRINT and CONFIG\\_SPL\\_BSS\\_MAX\\_SIZE must not be both defined at the same time.

### CONFIG\\_SPL\\_MAX\\_SIZE

Maximum size of the SPL image (text, data, rodata, and linker lists sections), BSS excluded.

When defined, the linker checks that the actual size does not exceed it.

### CONFIG\\_SPL\\_RELOC\\_TEXT\\_BASE

Address to relocate to. If unspecified, this is equal to CONFIG\\_SPL\\_TEXT\\_BASE (i.e. no relocation is done).

### CONFIG\\_SPL\\_BSS\\_START\\_ADDR

Link address for the BSS within the SPL binary.

### CONFIG\\_SPL\\_BSS\\_MAX\\_SIZE

Maximum size in memory allocated to the SPL BSS.

When defined, the linker checks that the actual memory used by SPL from `\_bss\_start` to `\_bss\_end` does not exceed it.

CONFIG\\_SPL\\_MAX\\_FOOTPRINT and CONFIG\\_SPL\\_BSS\\_MAX\\_SIZE



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must not be both defined at the same time.

### CONFIG\\_SPL\\_STACK

Address of the start of the stack SPL will use

### CONFIG\\_SPL\\_PANIC\\_ON\\_RAW\\_IMAGE

When defined, SPL will panic() if the image it has loaded does not have a signature.

Defining this is useful when code which loads images in SPL cannot guarantee that absolutely all read errors will be caught.

An example is the LPC32XX MLC NAND driver, which will consider that a completely unreadable NAND block is bad, and thus should be skipped silently.

### CONFIG\\_SPL\\_RELOC\\_STACK

Address of the start of the stack SPL will use after relocation. If unspecified, this is equal to CONFIG\\_SPL\\_STACK.

### CONFIG\\_SYS\\_SPL\\_MALLOC\\_START

Starting address of the malloc pool used in SPL.

When this option is set the full malloc is used in SPL and it is set up by spl\\_init() and before that, the simple malloc() can be used if CONFIG\\_SYS\\_MALLOC\\_F is defined.

### CONFIG\\_SYS\\_SPL\\_MALLOC\\_SIZE

The size of the malloc pool used in SPL.

### CONFIG\\_SPL\\_OS\\_BOOT

Enable booting directly to an OS from SPL.

See also: doc/README.falcon

### CONFIG\\_SPL\\_DISPLAY\\_PRINT

For ARM, enable an optional function to print more information about the running system.

### CONFIG\\_SPL\\_INIT\\_MINIMAL

Arch init code should be built for a very small image

### CONFIG\\_SYS\\_MMCSO\\_RAW\\_MODE\\_U\\_BOOT\\_PARTITION

Partition on the MMC to load U-Boot from when the MMC is being used in raw mode

### CONFIG\\_SYS\\_MMCSO\\_RAW\\_MODE\\_KERNEL\\_SECTOR

Sector to load kernel uImage from when MMC is being used in raw mode (for Falcon mode)

### CONFIG\\_SYS\\_MMCSO\\_RAW\\_MODE\\_ARGS\\_SECTOR,

### CONFIG\\_SYS\\_MMCSO\\_RAW\\_MODE\\_ARGS\\_SECTORS

Sector and number of sectors to load kernel argument parameters from when MMC is being used in raw mode (for falcon mode)

### CONFIG\\_SPL\\_FS\\_LOAD\\_PAYLOAD\\_NAME

Filename to read to load U-Boot when reading from filesystem

### CONFIG\\_SPL\\_FS\\_LOAD\\_KERNEL\\_NAME

Filename to read to load kernel uImage when reading from filesystem (for Falcon mode)

### CONFIG\\_SPL\\_FS\\_LOAD\\_ARGS\\_NAME

Filename to read to load kernel argument parameters when reading from filesystem (for Falcon mode)

### CONFIG\\_SPL\\_MPC83XX\\_WAIT\\_FOR\\_NAND

Set this for NAND SPL on PPC mpc83xx targets, so that start.S waits for the rest of the SPL to load before continuing (the hardware starts execution after just loading the first page rather than the full 4K).

### CONFIG\\_SPL\\_SKIP\\_RELOCATE

Avoid SPL relocation

### CONFIG\\_SPL\\_NAND\\_IDENT

SPL uses the chip ID list to identify the NAND flash. Requires CONFIG\\_SPL\\_NAND\\_BASE.



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### CONFIG\\_SPL\\_UBI

Support for a lightweight UBI (fastmap) scanner and loader

### CONFIG\\_SPL\\_NAND\\_RAW\\_ONLY

Support to boot only raw u-boot.bin images. Use this only if you need to save space.

### CONFIG\\_SPL\\_COMMON\\_INIT\\_DDR

Set for common ddr init with serial presence detect in SPL binary.

CONFIG\\_SYS\\_NAND\\_5\\_ADDR\\_CYCLE, CONFIG\\_SYS\\_NAND\\_PAGE\\_COUNT,  
CONFIG\\_SYS\\_NAND\\_PAGE\\_SIZE, CONFIG\\_SYS\\_NAND\\_OOBSIZE,  
CONFIG\\_SYS\\_NAND\\_BLOCK\\_SIZE, CONFIG\\_SYS\\_NAND\\_BAD\\_BLOCK\\_POS,  
CONFIG\\_SYS\\_NAND\\_ECCPOS, CONFIG\\_SYS\\_NAND\\_ECCSIZE,  
CONFIG\\_SYS\\_NAND\\_ECCBYTES

Defines the size and behavior of the NAND that SPL uses to read U-Boot

### CONFIG\\_SYS\\_NAND\\_U\\_BOOT\\_OFFS

Location in NAND to read U-Boot from

### CONFIG\\_SYS\\_NAND\\_U\\_BOOT\\_DST

Location in memory to load U-Boot to

### CONFIG\\_SYS\\_NAND\\_U\\_BOOT\\_SIZE

Size of image to load

### CONFIG\\_SYS\\_NAND\\_U\\_BOOT\\_START

Entry point in loaded image to jump to

### CONFIG\\_SYS\\_NAND\\_HW\\_ECC\\_OOBFIRST

Define this if you need to first read the OOB and then the data. This is used, for example, on davinci platforms.

### CONFIG\\_SPL\\_RAM\\_DEVICE

Support for running image already present in ram, in SPL binary

### CONFIG\\_SPL\\_PAD\\_TO

Image offset to which the SPL should be padded before appending the SPL payload. By default, this is defined as CONFIG\\_SPL\\_MAX\\_SIZE, or 0 if CONFIG\\_SPL\\_MAX\\_SIZE is undefined. CONFIG\\_SPL\\_PAD\\_TO must be either 0, meaning to append the SPL payload without any padding, or >= CONFIG\\_SPL\\_MAX\\_SIZE.

### CONFIG\\_SPL\\_TARGET

Final target image containing SPL and payload. Some SPLs use an arch-specific makefile fragment instead, for example if more than one image needs to be produced.

### CONFIG\\_SPL\\_FIT\\_PRINT

Printing information about a FIT image adds quite a bit of code to SPL. So this is normally disabled in SPL. Use this option to re-enable it. This will affect the output of the bootm command when booting a FIT image.

### - TPL framework

#### CONFIG\\_TPL

Enable building of TPL globally.

### CONFIG\\_TPL\\_PAD\\_TO

Image offset to which the TPL should be padded before appending the TPL payload. By default, this is defined as CONFIG\\_SPL\\_MAX\\_SIZE, or 0 if CONFIG\\_SPL\\_MAX\\_SIZE is undefined. CONFIG\\_SPL\\_PAD\\_TO must be either 0, meaning to append the SPL payload without any padding, or >= CONFIG\\_SPL\\_MAX\\_SIZE.

### - Interrupt support (PPC):

There are common interrupt\\_init() and timer\\_interrupt() for all PPC archs. interrupt\\_init() calls interrupt\\_init\\_cpu() for CPU specific initialization. interrupt\\_init\\_cpu() should set decremter\\_count to appropriate value. If CPU resets decremter automatically after interrupt (ppc4xx) it should set decremter\\_count to zero.



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timer\\_interrupt() calls timer\\_interrupt\\_cpu() for CPU specific handling. If board has watchdog / status\\_led / other\\_activity\\_monitor it works automatically from general timer\\_interrupt().

Board initialization settings:  
-----

During Initialization u-boot calls a number of board specific functions to allow the preparation of board specific prerequisites, e.g. pin setup before drivers are initialized. To enable these callbacks the following configuration macros have to be defined. Currently this is architecture specific, so please check arch/your\\_architecture/lib/board.c typically in board\\_init\\_f() and board\\_init\\_r().

- CONFIG\\_BOARD\\_EARLY\\_INIT\\_F: Call board\\_early\\_init\\_f()
- CONFIG\\_BOARD\\_EARLY\\_INIT\\_R: Call board\\_early\\_init\\_r()
- CONFIG\\_BOARD\\_LATE\\_INIT: Call board\\_late\\_init()
- CONFIG\\_BOARD\\_POSTCLK\\_INIT: Call board\\_postclk\\_init()

Configuration Settings:  
-----

- MEM\\_SUPPORT\\_64BIT\\_DATA: Defined automatically if compiled as 64-bit. Optionally it can be defined to support 64-bit memory commands.
- CONFIG\\_SYS\\_LONGHELP: Defined when you want long help messages included; undefine this when you're short of memory.
- CONFIG\\_SYS\\_HELP\\_CMD\\_WIDTH: Defined when you want to override the default width of the commands listed in the 'help' command output.
- CONFIG\\_SYS\\_PROMPT: This is what U-Boot prints on the console to prompt for user input.
- CONFIG\\_SYS\\_CBSIZE: Buffer size for input from the Console
- CONFIG\\_SYS\\_PBSIZE: Buffer size for Console output
- CONFIG\\_SYS\\_MAXARGS: max. Number of arguments accepted for monitor commands
- CONFIG\\_SYS\\_BARGSIZE: Buffer size for Boot Arguments which are passed to the application (usually a Linux kernel) when it is booted
- CONFIG\\_SYS\\_BAUDRATE\\_TABLE:  
List of legal baudrate settings for this board.
- CONFIG\\_SYS\\_MEM\\_RESERVE\\_SECURE  
Only implemented for ARMv8 for now.  
If defined, the size of CONFIG\\_SYS\\_MEM\\_RESERVE\\_SECURE memory is subtracted from total RAM and won't be reported to OS.  
This memory can be used as secure memory. A variable gd->arch.secure\\_ram is used to track the location. In systems the RAM base is not zero, or RAM is divided into banks, this variable needs to be recalculated to get the address.
- CONFIG\\_SYS\\_MEM\\_TOP\\_HIDE:  
If CONFIG\\_SYS\\_MEM\\_TOP\\_HIDE is defined in the board config header, this specified memory area will get subtracted from the top (end) of RAM and won't get 'touched' at all by U-Boot. By fixing up gd->ram\\_size the Linux kernel should get passed the now 'corrected' memory size and won't touch it either. This should work for arch/ppc and arch/powerpc. Only Linux board ports in arch/powerpc with bootwrapper support that recalculate the memory size from the SDRAM controller setup will have to get fixed in Linux additionally.  
  
This option can be used as a workaround for the 440EPx/GRx CHIP 11 errata where the last 256 bytes in SDRAM shouldn't be touched.

WARNING: Please make sure that this value is a multiple of the Linux page size (normally 4k). If this is not the case, then the end address of the Linux memory will be located at a non page size aligned address and this could cause major



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problems.

- CONFIG\\_SYS\\_LOADS\\_BAUD\\_CHANGE:  
Enable temporary baudrate change while serial download
- CONFIG\\_SYS\\_SDRAM\\_BASE:  
Physical start address of SDRAM. \\_Must\\_ be 0 here.
- CONFIG\\_SYS\\_FLASH\\_BASE:  
Physical start address of Flash memory.
- CONFIG\\_SYS\\_MONITOR\\_BASE:  
Physical start address of boot monitor code (set by make config files to be same as the text base address (CONFIG\\_SYS\\_TEXT\\_BASE) used when linking) - same as CONFIG\\_SYS\\_FLASH\\_BASE when booting from flash.
- CONFIG\\_SYS\\_MONITOR\\_LEN:  
Size of memory reserved for monitor code, used to determine \\_at\\_compile\\_time\\_ (!) if the environment is embedded within the U-Boot image, or in a separate flash sector.
- CONFIG\\_SYS\\_MALLOC\\_LEN:  
Size of DRAM reserved for malloc() use.
- CONFIG\\_SYS\\_MALLOC\\_F\\_LEN  
Size of the malloc() pool for use before relocation. If this is defined, then a very simple malloc() implementation will become available before relocation. The address is just below the global data, and the stack is moved down to make space.

This feature allocates regions with increasing addresses within the region. calloc() is supported, but realloc() is not available. free() is supported but does nothing. The memory will be freed (or in fact just forgotten) when U-Boot relocates itself.

- CONFIG\\_SYS\\_MALLOC\\_SIMPLE  
Provides a simple and small malloc() and calloc() for those boards which do not use the full malloc in SPL (which is enabled with CONFIG\\_SYS\\_SPL\\_MALLOC\\_START).

- CONFIG\\_SYS\\_NONCACHED\\_MEMORY:  
Size of non-cached memory area. This area of memory will be typically located right below the malloc() area and mapped uncached in the MMU. This is useful for drivers that would otherwise require a lot of explicit cache maintenance. For some drivers it's also impossible to properly maintain the cache. For example if the regions that need to be flushed are not a multiple of the cache-line size, \*and\* padding cannot be allocated between the regions to align them (i.e. if the HW requires a contiguous array of regions, and the size of each region is not cache-aligned), then a flush of one region may result in overwriting data that hardware has written to another region in the same cache-line. This can happen for example in network drivers where descriptors for buffers are typically smaller than the CPU cache-line (e.g. 16 bytes vs. 32 or 64 bytes).

Non-cached memory is only supported on 32-bit ARM at present.

- CONFIG\\_SYS\\_BOOTM\\_LEN:  
Normally compressed uImages are limited to an uncompressed size of 8 Mbytes. If this is not enough, you can define CONFIG\\_SYS\\_BOOTM\\_LEN in your board config file to adjust this setting to your needs.

- CONFIG\\_SYS\\_BOOTMAPSZ:  
Maximum size of memory mapped by the startup code of the Linux kernel; all data that must be processed by the Linux kernel (bd\\_info, boot arguments, FDT blob if used) must be put below this limit, unless 'bootm\\_low' environment variable is defined and non-zero. In such case all data for the Linux kernel must be between 'bootm\\_low' and 'bootm\\_low' + CONFIG\\_SYS\\_BOOTMAPSZ. The environment

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variable 'bootm\mapsize' will override the value of CONFIG\\_SYS\\_BOOTMAPSZ. If CONFIG\\_SYS\\_BOOTMAPSZ is undefined, then the value in 'bootm\\_size' will be used instead.

- CONFIG\\_SYS\\_BOOT\\_RAMDISK\\_HIGH:  
Enable initrd\\_high functionality. If defined then the initrd\\_high feature is enabled and the bootm ramdisk subcommand is enabled.

- CONFIG\\_SYS\\_BOOT\\_GET\\_CMDLINE:  
Enables allocating and saving kernel cmdline in space between 'bootm\\_low' and 'bootm\\_low' + BOOTMAPSZ.

- CONFIG\\_SYS\\_BOOT\\_GET\\_KBD:  
Enables allocating and saving a kernel copy of the bd\\_info in space between 'bootm\\_low' and 'bootm\\_low' + BOOTMAPSZ.

- CONFIG\\_SYS\\_MAX\\_FLASH\\_BANKS:  
Max number of Flash memory banks

- CONFIG\\_SYS\\_MAX\\_FLASH\\_SECT:  
Max number of sectors on a Flash chip

- CONFIG\\_SYS\\_FLASH\\_ERASE\\_TOUT:  
Timeout for Flash erase operations (in ms)

- CONFIG\\_SYS\\_FLASH\\_WRITE\\_TOUT:  
Timeout for Flash write operations (in ms)

- CONFIG\\_SYS\\_FLASH\\_LOCK\\_TOUT  
Timeout for Flash set sector lock bit operation (in ms)

- CONFIG\\_SYS\\_FLASH\\_UNLOCK\\_TOUT  
Timeout for Flash clear lock bits operation (in ms)

- CONFIG\\_SYS\\_FLASH\\_PROTECTION  
If defined, hardware flash sectors protection is used instead of U-Boot software protection.

- CONFIG\\_SYS\\_DIRECT\\_FLASH\\_TFTP:  
Enable TFTP transfers directly to flash memory; without this option such a download has to be performed in two steps: (1) download to RAM, and (2) copy from RAM to flash.

The two-step approach is usually more reliable, since you can check if the download worked before you erase the flash, but in some situations (when system RAM is too limited to allow for a temporary copy of the downloaded image) this option may be very useful.

- CONFIG\\_SYS\\_FLASH\\_CFI:  
Define if the flash driver uses extra elements in the common flash structure for storing flash geometry.

- CONFIG\\_FLASH\\_CFI\\_DRIVER  
This option also enables the building of the cfi\\_flash driver in the drivers directory

- CONFIG\\_FLASH\\_CFI\\_MTD  
This option enables the building of the cfi\\_mtd driver in the drivers directory. The driver exports CFI flash to the MTD layer.

- CONFIG\\_SYS\\_FLASH\\_USE\\_BUFFER\\_WRITE  
Use buffered writes to flash.

- CONFIG\\_FLASH\\_SPANSION\\_S29WS\\_N  
s29ws-n MirrorBit flash has non-standard addresses for buffered write commands.

- CONFIG\\_SYS\\_FLASH\\_QUIET\\_TEST  
If this option is defined, the common CFI flash doesn't print it's warning upon not recognized FLASH banks. This is useful, if some of the configured banks are only optionally available.



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### - CONFIG\\_FLASH\\_SHOW\\_PROGRESS

If defined (must be an integer), print out countdown digits and dots. Recommended value: 45 (9..1) for 80 column displays, 15 (3..1) for 40 column displays.

### - CONFIG\\_FLASH\\_VERIFY

If defined, the content of the flash (destination) is compared against the source after the write operation. An error message will be printed when the contents are not identical. Please note that this option is useless in nearly all cases, since such flash programming errors usually are detected earlier while unprotecting/erasing/programming. Please only enable this option if you really know what you are doing.

### - CONFIG\\_SYS\\_RX\\_ETH\\_BUFFER:

Defines the number of Ethernet receive buffers. On some Ethernet controllers it is recommended to set this value to 8 or even higher (EEPROM or 405 EMAC), since all buffers can be full shortly after enabling the interface on high Ethernet traffic. Defaults to 4 if not defined.

### - CONFIG\\_ENV\\_MAX\\_ENTRIES

Maximum number of entries in the hash table that is used internally to store the environment settings. The default setting is supposed to be generous and should work in most cases. This setting can be used to tune behaviour; see lib/hashtable.c for details.

### - CONFIG\\_ENV\\_FLAGS\\_LIST\\_DEFAULT

### - CONFIG\\_ENV\\_FLAGS\\_LIST\\_STATIC

Enable validation of the values given to environment variables when calling env set. Variables can be restricted to only decimal, hexadecimal, or boolean. If CONFIG\\_CMD\\_NET is also defined, the variables can also be restricted to IP address or MAC address.

The format of the list is:

```
type\_attribute = {\lbrack}s|d|x|b|i|m{\rbrack}  
access\_attribute = {\lbrack}a|r|o|c{\rbrack}  
attributes = type\_attribute{\lbrack}access\_attribute{\rbrack}  
entry = variable\_name{\lbrack}:attributes{\rbrack}  
list = entry{\lbrack},list{\rbrack}
```

The type attributes are:

- s - String (default)
- d - Decimal
- x - Hexadecimal
- b - Boolean ({\lbrack}1yYtT|0nNfF{\rbrack})
- i - IP address
- m - MAC address

The access attributes are:

- a - Any (default)
- r - Read-only
- o - Write-once
- c - Change-default

### - CONFIG\\_ENV\\_FLAGS\\_LIST\\_DEFAULT

Define this to a list (string) to define the ''flags'' environment variable in the default or embedded environment.

### - CONFIG\\_ENV\\_FLAGS\\_LIST\\_STATIC

Define this to a list (string) to define validation that should be done if an entry is not found in the ''flags'' environment variable. To override a setting in the static list, simply add an entry for the same variable name to the ''flags'' variable.

If CONFIG\\_REGEX is defined, the variable\\_name above is evaluated as a regular expression. This allows multiple variables to define the same flags without explicitly listing them for each variable.

The following definitions that deal with the placement and management of environment data (variable area); in general, we support the following configurations:



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### - CONFIG\\_BUILD\\_ENVCRC:

Builds up envcrc with the target environment so that external utils may easily extract it and embed it in final U-Boot images.

BE CAREFUL! The first access to the environment happens quite early in U-Boot initialization (when we try to get the setting of for the console baudrate). You *\*MUST\** have mapped your NVRAM area then, or U-Boot will hang.

Please note that even with NVRAM we still use a copy of the environment in RAM: we could work on NVRAM directly, but we want to keep settings there always unmodified except somebody uses `''saveenv''` to save the current settings.

BE CAREFUL! For some special cases, the local device can not use `''saveenv''` command. For example, the local device will get the environment stored in a remote NOR flash by SRIO or PCIE link, but it can not erase, write this NOR flash by SRIO or PCIE interface.

### - CONFIG\\_NAND\\_ENV\\_DST

Defines address in RAM to which the `nand\_spl` code should copy the environment. If redundant environment is used, it will be copied to `CONFIG\_NAND\_ENV\_DST + CONFIG\_ENV\_SIZE`.

Please note that the environment is read-only until the monitor has been relocated to RAM and a RAM copy of the environment has been created; also, when using EEPROM you will have to use `env\_get\_f()` until then to read environment variables.

The environment is protected by a CRC32 checksum. Before the monitor is relocated into RAM, as a result of a bad CRC you will be working with the compiled-in default environment - *\*silently\*!!!* `{\lbrack}`This is necessary, because the first environment variable we need is the `''baudrate''` setting for the console - if we have a bad CRC, we don't have any device yet where we could complain.`{\rbrack}`

Note: once the monitor has been relocated, then it will complain if the default environment is used; a new CRC is computed as soon as you use the `''saveenv''` command to store a valid environment.

### - CONFIG\\_SYS\\_FAULT\\_ECHO\\_LINK\\_DOWN:

Echo the inverted Ethernet link state to the fault LED.

Note: If this option is active, then `CONFIG\_SYS\_FAULT\_MII\_ADDR` also needs to be defined.

### - CONFIG\\_SYS\\_FAULT\\_MII\\_ADDR:

MII address of the PHY to check for the Ethernet link state.

### - CONFIG\\_NS16550\\_MIN\\_FUNCTIONS:

Define this if you desire to only have use of the `NS16550\_init` and `NS16550\_putc` functions for the serial driver located at `drivers/serial/ns16550.c`. This option is useful for saving space for already greatly restricted images, including but not limited to `NAND\_SPL` configurations.

### - CONFIG\\_DISPLAY\\_BOARDINFO

Display information about the board that U-Boot is running on when U-Boot starts up. The board function `checkboard()` is called to do this.

### - CONFIG\\_DISPLAY\\_BOARDINFO\\_LATE

Similar to the previous option, but display this information later, once `stdio` is running and output goes to the LCD, if present.

### - CONFIG\\_BOARD\\_SIZE\\_LIMIT:

Maximum size of the U-Boot image. When defined, the build system checks that the actual size does not exceed it.

Low Level (hardware related) configuration options:  
-----





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- CONFIG\\_SYS\\_CACHELINE\\_SIZE:  
Cache Line Size of the CPU.

- CONFIG\\_SYS\\_CCSRBAR\\_DEFAULT:  
Default (power-on reset) physical address of CCSR on Freescale PowerPC SOCs.

- CONFIG\\_SYS\\_CCSRBAR:  
Virtual address of CCSR. On a 32-bit build, this is typically the same value as CONFIG\\_SYS\\_CCSRBAR\\_DEFAULT.

- CONFIG\\_SYS\\_CCSRBAR\\_PHYS:  
Physical address of CCSR. CCSR can be relocated to a new physical address, if desired. In this case, this macro should be set to that address. Otherwise, it should be set to the same value as CONFIG\\_SYS\\_CCSRBAR\\_DEFAULT. For example, CCSR is typically relocated on 36-bit builds. It is recommended that this macro be defined via the \\_HIGH and \\_LOW macros:

```
\#define CONFIG\_SYS\_CCSRBAR\_PHYS ((CONFIG\_SYS\_CCSRBAR\_PHYS\_HIGH  
* 1ull)
```

- CONFIG\\_SYS\\_CCSRBAR\\_PHYS\\_HIGH:  
Bits 33-36 of CONFIG\\_SYS\\_CCSRBAR\\_PHYS. This value is typically either 0 (32-bit build) or 0xF (36-bit build). This macro is used in assembly code, so it must not contain typecasts or integer size suffixes (e.g. 'ULL').

- CONFIG\\_SYS\\_CCSRBAR\\_PHYS\\_LOW:  
Lower 32-bits of CONFIG\\_SYS\\_CCSRBAR\\_PHYS. This macro is used in assembly code, so it must not contain typecasts or integer size suffixes (e.g. 'ULL').

- CONFIG\\_SYS\\_CCSR\\_DO\\_NOT\\_RELOCATE:  
If this macro is defined, then CONFIG\\_SYS\\_CCSRBAR\\_PHYS will be forced to a value that ensures that CCSR is not relocated.

- CONFIG\\_IDE\\_AHB:  
Most IDE controllers were designed to be connected with PCI interface. Only few of them were designed for AHB interface. When software is doing ATA command and data transfer to IDE devices through IDE-AHB controller, some additional registers accessing to these kind of IDE-AHB controller is required.

- CONFIG\\_SYS\\_IMMR: Physical address of the Internal Memory.  
DO NOT CHANGE unless you know exactly what you're doing! (11-4) {\lbrack}MPC8xx systems only{\rbrack}

- CONFIG\\_SYS\\_INIT\\_RAM\\_ADDR:

Start address of memory area that can be used for initial data and stack; please note that this must be writable memory that is working WITHOUT special initialization, i. e. you CANNOT use normal RAM which will become available only after programming the memory controller and running certain initialization sequences.

U-Boot uses the following memory types:  
- MPC8xx: IMMR (internal memory of the CPU)

- CONFIG\\_SYS\\_GBL\\_DATA\\_OFFSET:

Offset of the initial data structure in the memory area defined by CONFIG\\_SYS\\_INIT\\_RAM\\_ADDR. Usually CONFIG\\_SYS\\_GBL\\_DATA\\_OFFSET is chosen such that the initial data is located at the end of the available space (sometimes written as (CONFIG\\_SYS\\_INIT\\_RAM\\_SIZE - GENERATED\\_GBL\\_DATA\\_SIZE), and the initial stack is just below that area (growing from (CONFIG\\_SYS\\_INIT\\_RAM\\_ADDR + CONFIG\\_SYS\\_GBL\\_DATA\\_OFFSET) downward.

Note:  
On the MPC824X (or other systems that use the data cache for initial memory) the address chosen for CONFIG\\_SYS\\_INIT\\_RAM\\_ADDR is basically arbitrary - it must



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point to an otherwise UNUSED address space between the top of RAM and the start of the PCI space.

- CONFIG\\_SYS\\_SCCR: System Clock and reset Control Register (15-27)

- CONFIG\\_SYS\\_OR\\_TIMING\\_SDRAM:  
SDRAM timing

- CONFIG\\_SYS\\_MAMR\\_PTA:  
periodic timer for refresh

- FLASH\\_BASE0\\_PRELIM, FLASH\\_BASE1\\_PRELIM, CONFIG\\_SYS\\_REMAP\\_OR\\_AM,  
CONFIG\\_SYS\\_PRELIM\\_OR\\_AM, CONFIG\\_SYS\\_OR\\_TIMING\\_FLASH, CONFIG\\_SYS\\_ORO\\_REMAP,  
CONFIG\\_SYS\\_ORO\\_PRELIM, CONFIG\\_SYS\\_BR0\\_PRELIM, CONFIG\\_SYS\\_OR1\\_REMAP, CONFIG\\_SYS\\_OR1\\_PRELIM,  
CONFIG\\_SYS\\_BR1\\_PRELIM:  
Memory Controller Definitions: BR0/1 and OR0/1 (FLASH)

- SDRAM\\_BASE2\\_PRELIM, SDRAM\\_BASE3\\_PRELIM, SDRAM\\_MAX\\_SIZE,  
CONFIG\\_SYS\\_OR\\_TIMING\\_SDRAM, CONFIG\\_SYS\\_OR2\\_PRELIM, CONFIG\\_SYS\\_BR2\\_PRELIM,  
CONFIG\\_SYS\\_OR3\\_PRELIM, CONFIG\\_SYS\\_BR3\\_PRELIM:  
Memory Controller Definitions: BR2/3 and OR2/3 (SDRAM)

- CONFIG\\_PCI\\_INDIRECT\\_BRIDGE:  
Enable support for indirect PCI bridges.

- CONFIG\\_SYS\\_SRIO:  
Chip has SRIO or not

- CONFIG\\_SRIO1:  
Board has SRIO 1 port available

- CONFIG\\_SRIO2:  
Board has SRIO 2 port available

- CONFIG\\_SRIO\\_PCIE\\_BOOT\\_MASTER  
Board can support master function for Boot from SRIO and PCIE

- CONFIG\\_SYS\\_SRIO<sub>n</sub>\\_MEM\\_VIRT:  
Virtual Address of SRIO port 'n' memory region

- CONFIG\\_SYS\\_SRIO<sub>n</sub>\\_MEM\\_PHYxS:  
Physical Address of SRIO port 'n' memory region

- CONFIG\\_SYS\\_SRIO<sub>n</sub>\\_MEM\\_SIZE:  
Size of SRIO port 'n' memory region

- CONFIG\\_SYS\\_NAND\\_BUSWIDTH\\_16BIT  
Defined to tell the NAND controller that the NAND chip is using a 16 bit bus.

Not all NAND drivers use this symbol.

Example of drivers that use it:

- drivers/mtd/nand/raw/ndfc.c
- drivers/mtd/nand/raw/mxc\\_nand.c

- CONFIG\\_SYS\\_NDFC\\_EBCO\\_CFG  
Sets the EBCO\\_CFG register for the NDFC. If not defined a default value will be used.

- CONFIG\\_SPD\\_EEPROM  
Get DDR timing information from an I2C EEPROM. Common with pluggable memory modules such as SODIMMs

SPD\\_EEPROM\\_ADDRESS  
I2C address of the SPD EEPROM

- CONFIG\\_SYS\\_SPD\\_BUS\\_NUM  
If SPD EEPROM is on an I2C bus other than the first one, specify here. Note that the value must resolve to something your driver can deal with.

- CONFIG\\_SYS\\_DDR\\_RAW\\_TIMING  
Get DDR timing information from other than SPD. Common with soldered DDR chips onboard without SPD. DDR raw timing parameters are extracted from datasheet and hard-coded into header files or board specific files.

- CONFIG\\_FSL\\_DDR\\_INTERACTIVE



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Enable interactive DDR debugging. See doc/README.fsl-ddr.

- CONFIG\\_FSL\\_DDR\\_SYNC\\_REFRESH  
Enable sync of refresh for multiple controllers.

- CONFIG\\_FSL\\_DDR\\_BIST  
Enable built-in memory test for Freescale DDR controllers.

- CONFIG\\_SYS\\_83XX\\_DDR\\_USES\\_CS0  
Only for 83xx systems. If specified, then DDR should be configured using CS0 and CS1 instead of CS2 and CS3.

- CONFIG\\_RMII  
Enable RMII mode for all FECs.  
Note that this is a global option, we can't have one FEC in standard MII mode and another in RMII mode.

- CONFIG\\_CRC32\\_VERIFY  
Add a verify option to the crc32 command.  
The syntax is:

```
=> crc32 -v
```

Where address/count indicate a memory area and crc32 is the correct crc32 which the area should have.

- CONFIG\\_LOOPW  
Add the ''loopw'' memory command. This only takes effect if the memory commands are activated globally (CONFIG\\_CMD\\_MEMORY).

- CONFIG\\_CMD\\_MX\\_CYCLIC  
Add the ''mdc'' and ''mwc'' memory commands. These are cyclic ''md/mw'' commands.  
Examples:

```
=> mdc.b 10 4 500  
This command will print 4 bytes (10,11,12,13) each 500 ms.
```

```
=> mwc.l 100 12345678 10  
This command will write 12345678 to address 100 all 10 ms.
```

This only takes effect if the memory commands are activated globally (CONFIG\\_CMD\\_MEMORY).

- CONFIG\\_SKIP\\_LOWLEVEL\\_INIT  
{\lbrack}ARM, NDS32, MIPS, RISC-V only{\rbrack} If this variable is defined, then certain low level initializations (like setting up the memory controller) are omitted and/or U-Boot does not relocate itself into RAM.

Normally this variable MUST NOT be defined. The only exception is when U-Boot is loaded (to RAM) by some other boot loader or by a debugger which performs these initializations itself.

- CONFIG\\_SKIP\\_LOWLEVEL\\_INIT\\_ONLY  
{\lbrack}ARM926EJ-S only{\rbrack} This allows just the call to lowlevel\\_init() to be skipped. The normal CP15 init (such as enabling the instruction cache) is still performed.

- CONFIG\\_SPL\\_BUILD  
Set when the currently-running compilation is for an artifact that will end up in the SPL (as opposed to the TPL or U-Boot proper). Code that needs stage-specific behavior should check this.

- CONFIG\\_TPL\\_BUILD  
Set when the currently-running compilation is for an artifact that will end up in the TPL (as opposed to the SPL or U-Boot proper). Code that needs stage-specific behavior should check this.

- CONFIG\\_SYS\\_MPC85XX\\_NO\\_RESETVEC  
Only for 85xx systems. If this variable is specified, the section .resetvec is not kept and the section .bootpg is placed in the previous 4k of the .text section.



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### - CONFIG\\_ARCH\\_MAP\\_SYSTEMEM

Generally U-Boot (and in particular the md command) uses effective address. It is therefore not necessary to regard U-Boot address as virtual addresses that need to be translated to physical addresses. However, sandbox requires this, since it maintains its own little RAM buffer which contains all addressable memory. This option causes some memory accesses to be mapped through map\\_systemem() / unmap\\_systemem().

### - CONFIG\\_X86\\_RESET\\_VECTOR

If defined, the x86 reset vector code is included. This is not needed when U-Boot is running from Coreboot.

### - CONFIG\\_SYS\\_NAND\\_NO\\_SUBPAGE\\_WRITE

Option to disable subpage write in NAND driver driver that uses this:  
drivers/mtd/nand/raw/davinci\\_nand.c

### ----- Freescale QE/FMAN Firmware Support: -----

The Freescale QUICCEngine (QE) and Frame Manager (FMAN) both support the loading of "firmware", which is encoded in the QE firmware binary format. This firmware often needs to be loaded during U-Boot booting, so macros are used to identify the storage device (NOR flash, SPI, etc) and the address within that device.

### - CONFIG\\_SYS\\_FMAN\\_FW\\_ADDR

The address in the storage device where the FMAN microcode is located. The meaning of this address depends on which CONFIG\\_SYS\\_QE\\_FMAN\\_FW\\_IN\\_xxx macro is also specified.

### - CONFIG\\_SYS\\_QE\\_FW\\_ADDR

The address in the storage device where the QE microcode is located. The meaning of this address depends on which CONFIG\\_SYS\\_QE\\_FMAN\\_FW\\_IN\\_xxx macro is also specified.

### - CONFIG\\_SYS\\_QE\\_FMAN\\_FW\\_LENGTH

The maximum possible size of the firmware. The firmware binary format has a field that specifies the actual size of the firmware, but it might not be possible to read any part of the firmware unless some local storage is allocated to hold the entire firmware first.

### - CONFIG\\_SYS\\_QE\\_FMAN\\_FW\\_IN\\_NOR

Specifies that QE/FMAN firmware is located in NOR flash, mapped as normal addressable memory via the LBC. CONFIG\\_SYS\\_FMAN\\_FW\\_ADDR is the virtual address in NOR flash.

### - CONFIG\\_SYS\\_QE\\_FMAN\\_FW\\_IN\\_NAND

Specifies that QE/FMAN firmware is located in NAND flash. CONFIG\\_SYS\\_FMAN\\_FW\\_ADDR is the offset within NAND flash.

### - CONFIG\\_SYS\\_QE\\_FMAN\\_FW\\_IN\\_MMC

Specifies that QE/FMAN firmware is located on the primary SD/MMC device. CONFIG\\_SYS\\_FMAN\\_FW\\_ADDR is the byte offset on that device.

### - CONFIG\\_SYS\\_QE\\_FMAN\\_FW\\_IN\\_REMOTE

Specifies that QE/FMAN firmware is located in the remote (master) memory space. CONFIG\\_SYS\\_FMAN\\_FW\\_ADDR is a virtual address which can be mapped from slave TLB->slave LAW->slave SRIIO or PCIE outbound window->master inbound window->master LAW->the ucode address in master's memory space.

### ----- Freescale Layerscape Management Complex Firmware Support: -----

The Freescale Layerscape Management Complex (MC) supports the loading of "firmware".

This firmware often needs to be loaded during U-Boot booting, so macros are used to identify the storage device (NOR flash, SPI, etc) and the address within that device.

### - CONFIG\\_FSL\\_MC\\_ENET

Enable the MC driver for Layerscape SoCs.

### ----- Freescale Layerscape Debug Server Support: -----



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The Freescale Layerscape Debug Server Support supports the loading of 'Debug Server firmware' and triggering SP boot-rom. This firmware often needs to be loaded during U-Boot booting.

- CONFIG\\_SYS\\_MC\\_RSV\\_MEM\\_ALIGN  
Define alignment of reserved memory MC requires

Reproducible builds

-----

In order to achieve reproducible builds, timestamps used in the U-Boot build process have to be set to a fixed value.

This is done using the SOURCE\\_DATE\\_EPOCH environment variable. SOURCE\\_DATE\\_EPOCH is to be set on the build host's shell, not as a configuration option for U-Boot or an environment variable in U-Boot.

SOURCE\\_DATE\\_EPOCH should be set to a number of seconds since the epoch, in UTC.

Building the Software:

=====

Building U-Boot has been tested in several native build environments and in many different cross environments. Of course we cannot support all possibly existing versions of cross development tools in all (potentially obsolete) versions. In case of tool chain problems we recommend to use the ELDK (see <https://www.denx.de/wiki/DULG/ELDK>) which is extensively used to build and test U-Boot.

If you are not using a native environment, it is assumed that you have GNU cross compiling tools available in your path. In this case, you must set the environment variable CROSS\\_COMPILE in your shell. Note that no changes to the Makefile or any other source files are necessary. For example using the ELDK on a 4xx CPU, please enter:

```
\$ CROSS\_COMPILE=ppc\_4xx-  
\$ export CROSS\_COMPILE
```

U-Boot is intended to be simple to build. After installing the sources you must configure U-Boot for one specific board type. This is done by typing:

```
make NAME\_defconfig
```

where 'NAME\\_defconfig' is the name of one of the existing configurations; see configs/\*\\_defconfig for supported names.

Note: for some boards special configuration names may exist; check if additional information is available from the board vendor; for instance, the TQM823L systems are available without (standard) or with LCD support. You can select such additional 'features' when choosing the configuration, i. e.

```
make TQM823L\_defconfig  
- will configure for a plain TQM823L, i. e. no LCD support
```

```
make TQM823L\_LCD\_defconfig  
- will configure for a TQM823L with U-Boot console on LCD
```

etc.

Finally, type 'make all', and you should get some working U-Boot images ready for download to / installation on your system:

```
- 'u-boot.bin' is a raw binary image  
- 'u-boot' is an image in ELF binary format  
- 'u-boot.srec' is in Motorola S-Record format
```

By default the build is performed locally and the objects are saved in the source directory. One of the two methods can be used to change this behavior and build U-Boot to some external directory:

1. Add O= to the make command line invocations:

```
make O=/tmp/build distclean  
make O=/tmp/build NAME\_defconfig
```



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```
make O=/tmp/build all
```

2. Set environment variable `KBUILD\_OUTPUT` to point to the desired location:

```
export KBUILD\_OUTPUT=/tmp/build
make distclean
make NAME\_defconfig
make all
```

Note that the command line `''O=''` setting overrides the `KBUILD\_OUTPUT` environment variable.

User specific `CPPFLAGS`, `AFLAGS` and `CFLAGS` can be passed to the compiler by setting the according environment variables `KCPPFLAGS`, `KAFLAGS` and `KCFLAGS`. For example to treat all compiler warnings as errors:

```
make KCFLAGS=-Werror
```

Please be aware that the Makefiles assume you are using GNU make, so for instance on NetBSD you might need to use `''gmake''` instead of native `''make''`.

If the system board that you have is not listed, then you will need to port U-Boot to your hardware platform. To do this, follow these steps:

1. Create a new directory to hold your board specific code. Add any files you need. In your board directory, you will need at least the `''Makefile''` and a `''c''`.
  2. Create a new configuration file `''include/configs/.h''` for your board.
  3. If you're porting U-Boot to a new CPU, then also create a new directory to hold your CPU specific code. Add any files you need.
  4. Run `''make \_defconfig''` with your new name.
  5. Type `''make''`, and you should get a working `''u-boot.srec''` file to be installed on your target system.
  6. Debug and solve any problems that might arise.
- {\lbrack}Of course, this last step is much harder than it sounds.{\rbrack}

Testing of U-Boot Modifications, Ports to New Hardware, etc.:

=====  
If you have modified U-Boot sources (for instance added a new board or support for new devices, a new CPU, etc.) you are expected to provide feedback to the other developers. The feedback normally takes the form of a `''patch''`, i.e. a context diff against a certain (latest official or latest in the git repository) version of U-Boot sources.

But before you submit such a patch, please verify that your modification did not break existing code. At least make sure that `*ALL*` of the supported boards compile WITHOUT ANY compiler warnings. To do so, just run the buildman script (`tools/buildman/buildman`), which will configure and build U-Boot for ALL supported system. Be warned, this will take a while. Please see the buildman README, or run `''buildman -H''` for documentation.

See also `''U-Boot Porting Guide''` below.

Monitor Commands - Overview:

=====  
`go` - start application at address `''addr''`  
`run` - run commands in an environment variable  
`bootm` - boot application image from memory  
`bootp` - boot image via network using BootP/TFTP protocol  
`bootz` - boot zImage from memory  
`tftpboot` - boot image via network using TFTP protocol  
and env variables `''ipaddr''` and `''serverip''`  
(and eventually `''gatewayip''`)  
`tftpboot` - upload a file via network using TFTP protocol  
`rarpboot` - boot image via network using RARP/TFTP protocol  
`diskboot` - boot from IDE devicebootd - boot default, i.e., run `''bootcmd''`  
`loads` - load S-Record file over serial line



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loadb - load binary file over serial line (kermit mode)  
md - memory display  
mm - memory modify (auto-incrementing)  
nm - memory modify (constant address)  
mw - memory write (fill)  
ms - memory search  
cp - memory copy  
cmp - memory compare  
crc32 - checksum calculation  
i2c - I2C sub-system  
sspi - SPI utility commands  
base - print or set address offset  
printenv - print environment variables  
pwm - control pwm channels  
setenv - set environment variables  
saveenv - save environment variables to persistent storage  
protect - enable or disable FLASH write protection  
erase - erase FLASH memory  
flinfo - print FLASH memory information  
nand - NAND memory operations (see doc/README.nand)  
bdinfo - print Board Info structure  
iminfo - print header information for application image  
coninfo - print console devices and informations  
ide - IDE sub-system  
loop - infinite loop on address range  
loopw - infinite write loop on address range  
mtest - simple RAM test  
icache - enable or disable instruction cache  
dcache - enable or disable data cache  
reset - Perform RESET of the CPU  
echo - echo args to console  
version - print monitor version  
help - print online help  
? - alias for 'help'

### Monitor Commands - Detailed Description:

=====  
TODO.

For now: just type ''help ''.

### Environment Variables:

=====  
U-Boot supports user configuration using Environment Variables which can be made persistent by saving to Flash memory.

Environment Variables are set using ''setenv'', printed using ''printenv'', and saved to Flash using ''saveenv''. Using ''setenv'' without a value can be used to delete a variable from the environment. As long as you don't save the environment you are working with an in-memory copy. In case the Flash area containing the environment is erased by accident, a default environment is provided.

Some configuration options can be set using Environment Variables.

List of environment variables (most likely not complete):

baudrate - see CONFIG\\_BAUDRATE

bootdelay - see CONFIG\\_BOOTDELAY

bootcmd - see CONFIG\\_BOOTCOMMAND

bootargs - Boot arguments when booting an RTOS image

bootfile - Name of the image to load with TFTP

bootm\\_low - Memory range available for image processing in the bootm command can be restricted. This variable is given as a hexadecimal number and defines lowest address allowed for use by the bootm command. See also ''bootm\\_size'' environment variable. Address defined by ''bootm\\_low'' is also the base of the initial memory mapping for the Linux



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kernel -- see the description of CONFIG\\_SYS\\_BOOTMAPSZ and bootm\\_mapsize.

bootm\\_mapsize - Size of the initial memory mapping for the Linux kernel. This variable is given as a hexadecimal number and it defines the size of the memory region starting at base address bootm\\_low that is accessible by the Linux kernel during early boot. If unset, CONFIG\\_SYS\\_BOOTMAPSZ is used as the default value if it is defined, and bootm\\_size is used otherwise.

bootm\\_size - Memory range available for image processing in the bootm command can be restricted. This variable is given as a hexadecimal number and defines the size of the region allowed for use by the bootm command. See also 'bootm\\_low' environment variable.

bootstopkeysha256, bootdelaykey, bootstopkey - See README.autoboot

updatefile - Location of the software update file on a TFTP server, used by the automatic software update feature. Please refer to documentation in doc/README.update for more details.

autoload - if set to 'no' (any string beginning with 'n'), 'bootp' will just load perform a lookup of the configuration from the BOOTP server, but not try to load any image using TFTP

autostart - if set to 'yes', an image loaded using the 'bootp', 'rarpboot', 'tftpboot' or 'diskboot' commands will be automatically started (by internally calling 'bootm')

If set to 'no', a standalone image passed to the 'bootm' command will be copied to the load address (and eventually uncompressed), but NOT be started. This can be used to load and uncompress arbitrary data.

fdt\\_high - if set this restricts the maximum address that the flattened device tree will be copied into upon boot. For example, if you have a system with 1 GB memory at physical address 0x10000000, while Linux kernel only recognizes the first 704 MB as low memory, you may need to set fdt\\_high as 0x3C000000 to have the device tree blob be copied to the maximum address of the 704 MB low memory, so that Linux kernel can access it during the boot procedure.

If this is set to the special value 0xFFFFFFFF then the fdt will not be copied at all on boot. For this to work it must reside in writable memory, have sufficient padding on the end of it for u-boot to add the information it needs into it, and the memory must be accessible by the kernel.

fdtcontroladdr- if set this is the address of the control flattened device tree used by U-Boot when CONFIG\\_OF\\_CONTROL is defined.

i2cfast - (PPC405GP|PPC405EP only)  
if set to 'y' configures Linux I2C driver for fast mode (400kHz). This environment variable is used in initialization code. So, for changes to be effective it must be saved and board must be reset.

initrd\\_high - restrict positioning of initrd images:  
If this variable is not set, initrd images will be copied to the highest possible address in RAM; this is usually what you want since it allows for maximum initrd size. If for some reason you want to make sure that the initrd image is loaded below the CONFIG\\_SYS\\_BOOTMAPSZ limit, you can set this environment variable to a value of 'no' or 'off' or '0'.  
Alternatively, you can set it to a maximum upper address to use (U-Boot will still check that it does not overwrite the U-Boot stack and data).





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For instance, when you have a system with 16 MB RAM, and want to reserve 4 MB from use by Linux, you can do this by adding 'mem=12M' to the value of the 'bootargs' variable. However, now you must make sure that the initrd image is placed in the first 12 MB as well - this can be done with

```
setenv initrd\_high 00c00000
```

If you set `initrd\_high` to `0xFFFFFFFF`, this is an indication to U-Boot that all addresses are legal for the Linux kernel, including addresses in flash memory. In this case U-Boot will NOT COPY the ramdisk at all. This may be useful to reduce the boot time on your system, but requires that this feature is supported by your Linux kernel.

`ipaddr` - IP address; needed for `tftpboot` command

`loadaddr` - Default load address for commands like 'bootp', 'rarpboot', 'tftpboot', 'loadb' or 'diskboot'

`loads\_echo` - see `CONFIG\_LOADS\_ECHO`

`serverip` - TFTP server IP address; needed for `tftpboot` command

`bootretry` - see `CONFIG\_BOOT\_RETRY\_TIME`

`bootdelaykey` - see `CONFIG\_AUTOBOOT\_DELAY\_STR`

`bootstopkey` - see `CONFIG\_AUTOBOOT\_STOP\_STR`

`ethprime` - controls which interface is used first.

`ethact` - controls which interface is currently active.  
For example you can do the following

```
=> setenv ethact FEC
=> ping 192.168.0.1 \# traffic sent on FEC
=> setenv ethact SCC
=> ping 10.0.0.1 \# traffic sent on SCC
```

`ethrotate` - When set to 'no' U-Boot does not go through all available network interfaces.  
It just stays at the currently selected interface.

`netretry` - When set to 'no' each network operation will either succeed or fail without retrying.  
When set to 'once' the network operation will fail when all the available network interfaces are tried once without success.  
Useful on scripts which control the retry operation themselves.

`npe\_uicode` - set load address for the NPE microcode

`silent\_linux` - If set then Linux will be told to boot silently, by changing the console to be empty. If 'yes' it will be made silent. If 'no' it will not be made silent. If unset, then it will be made silent if the U-Boot console is silent.

`tftpsrcp` - If this is set, the value is used for TFTP's UDP source port.

`tftpdstp` - If this is set, the value is used for TFTP's UDP destination port instead of the Well Know Port 69.

`tftpblocksize` - Block size to use for TFTP transfers; if not set, we use the TFTP server's default block size

`tftptimeout` - Retransmission timeout for TFTP packets (in milliseconds, minimum value is 1000 = 1 second). Defines when a packet is considered to be lost so it has to be retransmitted. The default is 5000 = 5 seconds.  
Lowering this value may make downloads succeed



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faster in networks with high packet loss rates or with unreliable TFTP servers.

tftptimeoutcountmax - maximum count of TFTP timeouts (no unit, minimum value = 0). Defines how many timeouts can happen during a single file transfer before that transfer is aborted. The default is 10, and 0 means 'no timeouts allowed'. Increasing this value may help downloads succeed with high packet loss rates, or with unreliable TFTP servers or client hardware.

tftpwindowsize - if this is set, the value is used for TFTP's window size as described by RFC 7440. This means the count of blocks we can receive before sending ack to server.

vlan - When set to a value < 4095 the traffic over Ethernet is encapsulated/received over 802.1q VLAN tagged frames.

bootpretryperiod - Period during which BOOTP/DHCP sends retries. Unsigned value, in milliseconds. If not set, the period will be either the default (28000), or a value based on CONFIG\\_NET\\_RETRY\\_COUNT, if defined. This value has precedence over the value based on CONFIG\\_NET\\_RETRY\\_COUNT.

memmatches - Number of matches found by the last 'ms' command, in hex

memaddr - Address of the last match found by the 'ms' command, in hex, or 0 if none

mempos - Index position of the last match found by the 'ms' command, in units of the size (.b, .w, .l) of the search

zbootbase - (x86 only) Base address of the bzImage 'setup' block

zbootaddr - (x86 only) Address of the loaded bzImage, typically BZIMAGE\\_LOAD\\_ADDR which is 0x100000

The following image location variables contain the location of images used in booting. The 'Image' column gives the role of the image and is not an environment variable name. The other columns are environment variable names. 'File Name' gives the name of the file on a TFTP server, 'RAM Address' gives the location in RAM the image will be loaded to, and 'Flash Location' gives the image's address in NOR flash or offset in NAND flash.

\*Note\* - these variables don't have to be defined for all boards, some boards currently use other variables for these purposes, and some boards use these variables for other purposes.

Image	File Name	RAM Address	Flash Location
u-boot	u-boot	u-boot\_addr\_r	u-boot\_addr
Linux kernel	bootfile	kernel\_addr\_r	kernel\_addr
device tree blob	fdtfile	fdt\_addr\_r	fdt\_addr
ramdisk	ramdiskfile	ramdisk\_addr\_r	ramdisk\_addr

The following environment variables may be used and automatically updated by the network boot commands ('bootp' and 'rarpboot'), depending on the information provided by your boot server:

bootfile - see above  
dnsip - IP address of your Domain Name Server  
dnsip2 - IP address of your secondary Domain Name Server  
gatewayip - IP address of the Gateway (Router) to use  
hostname - Target hostname  
ipaddr - see above  
netmask - Subnet Mask  
rootpath - Pathname of the root filesystem on the NFS server  
serverip - see above

There are two special Environment Variables:

serial\# - contains hardware identification information such as type string and/or serial number



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ethaddr - Ethernet address

These variables can be set only once (usually during manufacturing of the board). U-Boot refuses to delete or overwrite these variables once they have been set once.

Further special Environment Variables:

ver - Contains the U-Boot version string as printed with the 'version' command. This variable is readonly (see CONFIG\\_VERSION\\_VARIABLE).

Please note that changes to some configuration parameters may take only effect after the next boot (yes, that's just like Windoze :-).

Callback functions for environment variables:  
-----

For some environment variables, the behavior of u-boot needs to change when their values are changed. This functionality allows functions to be associated with arbitrary variables. On creation, overwrite, or deletion, the callback will provide the opportunity for some side effect to happen or for the change to be rejected.

The callbacks are named and associated with a function using the U\\_BOOT\\_ENV\\_CALLBACK macro in your board or driver code.

These callbacks are associated with variables in one of two ways. The static list can be added to by defining CONFIG\\_ENV\\_CALLBACK\\_LIST\\_STATIC in the board configuration to a string that defines a list of associations. The list must be in the following format:

```
entry = variable\_name{\lbrack}:callback\_name{\rbrack}
list = entry{\lbrack},list{\rbrack}
```

If the callback name is not specified, then the callback is deleted. Spaces are also allowed anywhere in the list.

Callbacks can also be associated by defining the '.callbacks' variable with the same list format above. Any association in '.callbacks' will override any association in the static list. You can define CONFIG\\_ENV\\_CALLBACK\\_LIST\\_DEFAULT to a list (string) to define the '.callbacks' environment variable in the default or embedded environment.

If CONFIG\\_REGEX is defined, the variable\\_name above is evaluated as a regular expression. This allows multiple variables to be connected to the same callback without explicitly listing them all out.

The signature of the callback functions is:

```
int callback(const char *name, const char *value, enum env\_op op, int flags)
```

```
* name - changed environment variable
* value - new value of the environment variable
* op - operation (create, overwrite, or delete)
* flags - attributes of the environment variable change, see flags H\_* in
include/search.h
```

The return value is 0 if the variable change is accepted and 1 otherwise.

Command Line Parsing:  
=====

There are two different command line parsers available with U-Boot: the old 'simple' one, and the much more powerful 'hush' shell:

Old, simple command line parser:  
-----

```
- supports environment variables (through setenv / saveenv commands)
- several commands on one line, separated by ';'
- variable substitution using '... \${name} ...' syntax
- special characters ('\$', ',') can be escaped by prefixing with '\textbackslash{}',
for example:
```



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```
setenv bootcmd bootm \textbackslash{\$}{address}
```

- You can also escape text by enclosing in single apostrophes, for example:

```
setenv addip 'setenv bootargs \bootargs ip=\$ipaddr:\$serverip:\$gatewayip:\$netmask:\$hostname::off'
```

Hush shell:

-----

- similar to Bourne shell, with control structures like

if...then...else...fi, for...do...done; while...do...done,

until...do...done, ...

- supports environment (''global'') variables (through setenv / saveenv

commands) and local shell variables (through standard shell syntax

''name=value''); only environment variables can be used with ''run''

command

General rules:

-----

(1) If a command line (or an environment variable executed by a ''run'' command) contains several commands separated by semicolon, and one of these commands fails, then the remaining commands will be executed anyway.

(2) If you execute several variables with one call to run (i. e. calling run with a list of variables as arguments), any failing command will cause ''run'' to terminate, i. e. the remaining variables are not executed.

Note for Redundant Ethernet Interfaces:

=====

Some boards come with redundant Ethernet interfaces; U-Boot supports such configurations and is capable of automatic selection of a ''working'' interface when needed. MAC assignment works as follows:

Network interfaces are numbered eth0, eth1, eth2, ... Corresponding MAC addresses can be stored in the environment as ''ethaddr'' (=>eth0), ''eth1addr'' (=>eth1), ''eth2addr'', ...

If the network interface stores some valid MAC address (for instance in SRROM), this is used as default address if there is NO corresponding setting in the environment; if the corresponding environment variable is set, this overrides the settings in the card; that means:

o If the SRROM has a valid MAC address, and there is no address in the environment, the SRROM's address is used.

o If there is no valid address in the SRROM, and a definition in the environment exists, then the value from the environment variable is used.

o If both the SRROM and the environment contain a MAC address, and both addresses are the same, this MAC address is used.

o If both the SRROM and the environment contain a MAC address, and the addresses differ, the value from the environment is used and a warning is printed.

o If neither SRROM nor the environment contain a MAC address, an error is raised. If CONFIG\\_NET\\_RANDOM\\_ETHADDR is defined, then in this case a random, locally-assigned MAC is used.

If Ethernet drivers implement the 'write\\_hwaddr' function, valid MAC addresses will be programmed into hardware as part of the initialization process. This may be skipped by setting the appropriate 'ethmacskip' environment variable. The naming convention is as follows:

''ethmacskip'' (=>eth0), ''eth1macskip'' (=>eth1) etc.

Image Formats:

=====

U-Boot is capable of booting (and performing other auxiliary operations on) images in two formats:

New uImage format (FIT)

-----



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Flexible and powerful format based on Flattened Image Tree -- FIT (similar to Flattened Device Tree). It allows the use of images with multiple components (several kernels, ramdisks, etc.), with contents protected by SHA1, MD5 or CRC32. More details are found in the doc/uImage.FIT directory.

### Old uImage format

Old image format is based on binary files which can be basically anything, preceded by a special header; see the definitions in include/image.h for details; basically, the header defines the following image properties:

- \* Target Operating System (Provisions for OpenBSD, NetBSD, FreeBSD, 4.4BSD, Linux, SVR4, Esix, Solaris, Irix, SCO, Dell, NCR, VxWorks, LynxOS, pSOS, QNX, RTEMS, INTEGRITY; Currently supported: Linux, NetBSD, VxWorks, QNX, RTEMS, LynxOS, INTEGRITY).
- \* Target CPU Architecture (Provisions for Alpha, ARM, Intel x86, IA64, MIPS, NDS32, Nios II, PowerPC, IBM S390, SuperH, Sparc, Sparc 64 Bit; Currently supported: ARM, Intel x86, MIPS, NDS32, Nios II, PowerPC).
- \* Compression Type (uncompressed, gzip, bzip2)
- \* Load Address
- \* Entry Point
- \* Image Name
- \* Image Timestamp

The header is marked by a special Magic Number, and both the header and the data portions of the image are secured against corruption by CRC32 checksums.

### Linux Support:

Although U-Boot should support any OS or standalone application easily, the main focus has always been on Linux during the design of U-Boot.

U-Boot includes many features that so far have been part of some special 'boot loader' code within the Linux kernel. Also, any 'initrd' images to be used are no longer part of one big Linux image; instead, kernel and 'initrd' are separate images. This implementation serves several purposes:

- the same features can be used for other OS or standalone applications (for instance: using compressed images to reduce the Flash memory footprint)
- it becomes much easier to port new Linux kernel versions because lots of low-level, hardware dependent stuff are done by U-Boot
- the same Linux kernel image can now be used with different 'initrd' images; of course this also means that different kernel images can be run with the same 'initrd'. This makes testing easier (you don't have to build a new 'zImage.initrd' Linux image when you just change a file in your 'initrd'). Also, a field-upgrade of the software is easier now.

### Linux HOWTO:

#### Porting Linux to U-Boot based systems:

U-Boot cannot save you from doing all the necessary modifications to configure the Linux device drivers for use with your target hardware (no, we don't intend to provide a full virtual machine interface to Linux :-).

But now you can ignore ALL boot loader code (in arch/powerpc/mbxboot).

Just make sure your machine specific header file (for instance include/asm-ppc/tqm8xx.h) includes the same definition of the Board Information structure as we define in include/asm-u-boot.h, and make sure that your definition of IMAP\_ADDR uses the same value



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as your U-Boot configuration in CONFIG\\_SYS\\_IMMR.

Note that U-Boot now has a driver model, a unified model for drivers. If you are adding a new driver, plumb it into driver model. If there is no uclass available, you are encouraged to create one. See doc/driver-model.

### Configuring the Linux kernel:

-----  
No specific requirements for U-Boot. Make sure you have some root device (initial ramdisk, NFS) for your target system.

### Building a Linux Image:

-----  
With U-Boot, 'normal' build targets like 'zImage' or 'bzImage' are not used. If you use recent kernel source, a new build target 'uImage' will exist which automatically builds an image usable by U-Boot. Most older kernels also have support for a 'pImage' target, which was introduced for our predecessor project PPCBoot and uses a 100% compatible format.

#### Example:

```
make TQM850L\_defconfig
make oldconfig
make dep
make uImage
```

The 'uImage' build target uses a special tool (in 'tools/mkimage') to encapsulate a compressed Linux kernel image with header information, CRC32 checksum etc. for use with U-Boot. This is what we are doing:

\* build a standard 'vmlinux' kernel image (in ELF binary format):

\* convert the kernel into a raw binary image:

```
\${CROSS}\_COMPILE\}-objcopy -O binary \textbackslash{}
-R .note -R .comment \textbackslash{}
-S vmlinux linux.bin
```

\* compress the binary image:

```
gzip -9 linux.bin
```

\* package compressed binary image for U-Boot:

```
mkimage -A ppc -O linux -T kernel -C gzip \textbackslash{}
-a 0 -e 0 -n 'Linux Kernel Image' \textbackslash{}
-d linux.bin.gz uImage
```

The 'mkimage' tool can also be used to create ramdisk images for use with U-Boot, either separated from the Linux kernel image, or combined into one file. 'mkimage' encapsulates the images with a 64 byte header containing information about target architecture, operating system, image type, compression method, entry points, time stamp, CRC32 checksums, etc.

'mkimage' can be called in two ways: to verify existing images and print the header information, or to build new images.

In the first form (with '-l' option) mkimage lists the information contained in the header of an existing U-Boot image; this includes checksum verification:

```
tools/mkimage -l image
-l ==> list image header information
```

The second form (with '-d' option) is used to build a U-Boot image from a 'data file' which is used as image payload:

```
tools/mkimage -A arch -O os -T type -C comp -a addr -e ep \textbackslash{}
-n name -d data\_file image
```



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```
-A ==> set architecture to 'arch'  
-O ==> set operating system to 'os'  
-T ==> set image type to 'type'  
-C ==> set compression type 'comp'  
-a ==> set load address to 'addr' (hex)  
-e ==> set entry point to 'ep' (hex)  
-n ==> set image name to 'name'  
-d ==> use image data from 'datafile'
```

Right now, all Linux kernels for PowerPC systems use the same load address (0x00000000), but the entry point address depends on the kernel version:

- 2.2.x kernels have the entry point at 0x0000000C,
- 2.3.x and later kernels have the entry point at 0x00000000.

So a typical call to build a U-Boot image would read:

```
-> tools/mkimage -n '2.4.4 kernel for TQM850L' \textbackslash{}  
> -A ppc -O linux -T kernel -C gzip -a 0 -e 0 \textbackslash{}  
> -d /opt/elsk/ppc\_8xx/usr/src/linux-2.4.4/arch/powerpc/coffboot/vmlinux.gz \textbackslash{}  
> examples/uImage.TQM850L  
Image Name: 2.4.4 kernel for TQM850L  
Created: Wed Jul 19 02:34:59 2000  
Image Type: PowerPC Linux Kernel Image (gzip compressed)  
Data Size: 335725 Bytes = 327.86 kB = 0.32 MB  
Load Address: 0x00000000  
Entry Point: 0x00000000
```

To verify the contents of the image (or check for corruption):

```
-> tools/mkimage -l examples/uImage.TQM850L  
Image Name: 2.4.4 kernel for TQM850L  
Created: Wed Jul 19 02:34:59 2000  
Image Type: PowerPC Linux Kernel Image (gzip compressed)  
Data Size: 335725 Bytes = 327.86 kB = 0.32 MB  
Load Address: 0x00000000  
Entry Point: 0x00000000
```

NOTE: for embedded systems where boot time is critical you can trade speed for memory and install an UNCOMPRESSED image instead: this needs more space in Flash, but boots much faster since it does not need to be uncompressed:

```
-> gunzip /opt/elsk/ppc\_8xx/usr/src/linux-2.4.4/arch/powerpc/coffboot/vmlinux.gz  
-> tools/mkimage -n '2.4.4 kernel for TQM850L' \textbackslash{}  
> -A ppc -O linux -T kernel -C none -a 0 -e 0 \textbackslash{}  
> -d /opt/elsk/ppc\_8xx/usr/src/linux-2.4.4/arch/powerpc/coffboot/vmlinux \textbackslash{}  
> examples/uImage.TQM850L-uncompressed  
Image Name: 2.4.4 kernel for TQM850L  
Created: Wed Jul 19 02:34:59 2000  
Image Type: PowerPC Linux Kernel Image (uncompressed)  
Data Size: 792160 Bytes = 773.59 kB = 0.76 MB  
Load Address: 0x00000000  
Entry Point: 0x00000000
```

Similar you can build U-Boot images from a 'ramdisk.image.gz' file when your kernel is intended to use an initial ramdisk:

```
-> tools/mkimage -n 'Simple Ramdisk Image' \textbackslash{}  
> -A ppc -O linux -T ramdisk -C gzip \textbackslash{}  
> -d /LinuxPPC/images/SIMPLE-ramdisk.image.gz examples/simple-initrd  
Image Name: Simple Ramdisk Image  
Created: Wed Jan 12 14:01:50 2000  
Image Type: PowerPC Linux RAMDisk Image (gzip compressed)  
Data Size: 566530 Bytes = 553.25 kB = 0.54 MB  
Load Address: 0x00000000  
Entry Point: 0x00000000
```

The 'dumpimage' tool can be used to disassemble or list the contents of images built by mkimage. See dumpimage's help output (-h) for details.

Installing a Linux Image:  
-----

To downloading a U-Boot image over the serial (console) interface,



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you must convert the image to S-Record format:

```
objcopy -I binary -O srec examples/image examples/image.srec
```

The 'objcopy' does not understand the information in the U-Boot image header, so the resulting S-Record file will be relative to address 0x00000000. To load it to a given address, you need to specify the target address as 'offset' parameter with the 'loads' command.

Example: install the image to address 0x40100000 (which on the TQM8xxL is in the first Flash bank):

```
=> erase 40100000 401FFFFFF
```

```
..... done  
Erased 8 sectors
```

```
=> loads 40100000  
\#\# Ready for S-Record download ...  
\textasciitilde{}>examples/image.srec  
1 2 3 4 5 6 7 8 9 10 11 12 13 ...  
...  
15989 15990 15991 15992  
{\lbrack}file transfer complete{\rbrack}  
{\lbrack}connected{\rbrack}  
\#\# Start Addr = 0x00000000
```

You can check the success of the download using the 'iminfo' command; this includes a checksum verification so you can be sure no data corruption happened:

```
=> imi 40100000
```

```
\#\# Checking Image at 40100000 ...  
Image Name: 2.2.13 for initrd on TQM850L  
Image Type: PowerPC Linux Kernel Image (gzip compressed)  
Data Size: 335725 Bytes = 327 kB = 0 MB  
Load Address: 00000000  
Entry Point: 0000000c  
Verifying Checksum ... OK
```

```
Boot Linux:  
-----
```

The 'bootm' command is used to boot an application that is stored in memory (RAM or Flash). In case of a Linux kernel image, the contents of the 'bootargs' environment variable is passed to the kernel as parameters. You can check and modify this variable using the 'printenv' and 'setenv' commands:

```
=> printenv bootargs  
bootargs=root=/dev/ram
```

```
=> setenv bootargs root=/dev/nfs rw nfsroot=10.0.0.2:/LinuxPPC nfsaddrs=10.0.0.99:10.0.0.2
```

```
=> printenv bootargs  
bootargs=root=/dev/nfs rw nfsroot=10.0.0.2:/LinuxPPC nfsaddrs=10.0.0.99:10.0.0.2
```

```
=> bootm 40020000  
\#\# Booting Linux kernel at 40020000 ...  
Image Name: 2.2.13 for NFS on TQM850L  
Image Type: PowerPC Linux Kernel Image (gzip compressed)  
Data Size: 381681 Bytes = 372 kB = 0 MB  
Load Address: 00000000  
Entry Point: 0000000c  
Verifying Checksum ... OK  
Uncompressing Kernel Image ... OK  
Linux version 2.2.13 (wd@denx.local.net) (gcc version 2.95.2 19991024 (release)) \#1 Wed Jul 19 02:35:17 MEST 2000  
Boot arguments: root=/dev/nfs rw nfsroot=10.0.0.2:/LinuxPPC nfsaddrs=10.0.0.99:10.0.0.2  
time\_init: decremter frequency = 187500000/60  
Calibrating delay loop... 49.77 BogoMIPS  
Memory: 15208k available (700k kernel code, 444k data, 32k init) {\lbrack}c0000000,c1000000{\rbrack}  
...
```





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If you want to boot a Linux kernel with initial RAM disk, you pass the memory addresses of both the kernel and the initrd image (PPBCOOT format!) to the ''bootm'' command:

```
=> imi 40100000 40200000
```

```
\#\# Checking Image at 40100000 ...
Image Name: 2.2.13 for initrd on TQM850L
Image Type: PowerPC Linux Kernel Image (gzip compressed)
Data Size: 335725 Bytes = 327 kB = 0 MB
Load Address: 00000000
Entry Point: 0000000c
Verifying Checksum ... OK
```

```
\#\# Checking Image at 40200000 ...
Image Name: Simple Ramdisk Image
Image Type: PowerPC Linux RAMDisk Image (gzip compressed)
Data Size: 566530 Bytes = 553 kB = 0 MB
Load Address: 00000000
Entry Point: 00000000
Verifying Checksum ... OK
```

```
=> bootm 40100000 40200000
```

```
\#\# Booting Linux kernel at 40100000 ...
Image Name: 2.2.13 for initrd on TQM850L
Image Type: PowerPC Linux Kernel Image (gzip compressed)
Data Size: 335725 Bytes = 327 kB = 0 MB
Load Address: 00000000
Entry Point: 0000000c
Verifying Checksum ... OK
Uncompressing Kernel Image ... OK
\#\# Loading RAMDisk Image at 40200000 ...
Image Name: Simple Ramdisk Image
Image Type: PowerPC Linux RAMDisk Image (gzip compressed)
Data Size: 566530 Bytes = 553 kB = 0 MB
Load Address: 00000000
Entry Point: 00000000
Verifying Checksum ... OK
Loading Ramdisk ... OK
Linux version 2.2.13 (wd@denx.local.net) (gcc version 2.95.2 19991024 (release)) \#1 Wed Jul 19 02:32:08 MEST 2000
Boot arguments: root=/dev/ram
time\_init: decremter frequency = 187500000/60
Calibrating delay loop... 49.77 BogoMIPS
...
RAMDISK: Compressed image found at block 0
VFS: Mounted root (ext2 filesystem).
```

```
bash\#
```

Boot Linux and pass a flat device tree:

-----

First, U-Boot must be compiled with the appropriate defines. See the section titled ''Linux Kernel Interface'' above for a more in depth explanation. The following is an example of how to start a kernel and pass an updated flat device tree:

```
=> print oftaddr
oftaddr=0x300000
=> print oft
oft=oftrees/mpc8540ads.dtb
=> tftp \oftaddr \oft
Speed: 1000, full duplex
Using TSECO device
TFTP from server 192.168.1.1; our IP address is 192.168.1.101
Filename 'oftrees/mpc8540ads.dtb'.
Load address: 0x300000
Loading: \#
done
Bytes transferred = 4106 (100a hex)
=> tftp \loadaddr \bootfile
Speed: 1000, full duplex
Using TSECO device
TFTP from server 192.168.1.1; our IP address is 192.168.1.2
Filename 'uImage'.
Load address: 0x200000
```



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```
Loading:\#\#\#\#\#\#\#\#\#\#\#\#\#\#\#\#
done
Bytes transferred = 1029407 (fb51f hex)
=> print loadaddr
loadaddr=200000
=> print oftaddr
oftaddr=0x300000
=> bootm \loadaddr - \oftaddr
\#\# Booting image at 00200000 ...
Image Name: Linux-2.6.17-dirty
Image Type: PowerPC Linux Kernel Image (gzip compressed)
Data Size: 1029343 Bytes = 1005.2 kB
Load Address: 00000000
Entry Point: 00000000
Verifying Checksum ... OK
Uncompressing Kernel Image ... OK
Booting using flat device tree at 0x300000
Using MPC85xx ADS machine description
Memory CAM mapping: CAM0=256Mb, CAM1=256Mb, CAM2=0Mb residual: 0Mb
{\lbrack}snip{\rbrack}
```

More About U-Boot Image Types:

-----  
U-Boot supports the following image types:

''Standalone Programs'' are directly runnable in the environment provided by U-Boot; it is expected that (if they behave well) you can continue to work in U-Boot after return from the Standalone Program.

''OS Kernel Images'' are usually images of some Embedded OS which will take over control completely. Usually these programs will install their own set of exception handlers, device drivers, set up the MMU, etc. - this means, that you cannot expect to re-enter U-Boot except by resetting the CPU.

''RAMDisk Images'' are more or less just data blocks, and their parameters (address, size) are passed to an OS kernel that is being started.

''Multi-File Images'' contain several images, typically an OS (Linux) kernel image and one or more data images like RAMDisks. This construct is useful for instance when you want to boot over the network using BOOTP etc., where the boot server provides just a single image file, but you want to get for instance an OS kernel and a RAMDisk image.

''Multi-File Images'' start with a list of image sizes, each image size (in bytes) specified by an ''uint32\t'' in network byte order. This list is terminated by an ''(uint32\t)0''. Immediately after the terminating 0 follow the images, one by one, all aligned on ''uint32\t'' boundaries (size rounded up to a multiple of 4 bytes).

''Firmware Images'' are binary images containing firmware (like U-Boot or FPGA images) which usually will be programmed to flash memory.

''Script files'' are command sequences that will be executed by U-Boot's command interpreter; this feature is especially useful when you configure U-Boot to use a real shell (hush) as command interpreter.

Booting the Linux zImage:

-----  
On some platforms, it's possible to boot Linux zImage. This is done using the ''bootz'' command. The syntax of ''bootz'' command is the same as the syntax of ''bootm'' command.

Note, defining the CONFIG\_SUPPORT\_RAW\_INITRD allows user to supply kernel with raw initrd images. The syntax is slightly different, the address of the initrd must be augmented by it's size, in the following format: '':''.

Standalone HOWTO:

=====



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One of the features of U-Boot is that you can dynamically load and run 'standalone' applications, which can use some resources of U-Boot like console I/O functions or interrupt services.

Two simple examples are included with the sources:

'Hello World' Demo:

-----

'examples/hello\\_world.c' contains a small 'Hello World' Demo application; it is automatically compiled when you build U-Boot. It's configured to run at address 0x00040004, so you can play with it like that:

```
=> loads
\#\# Ready for S-Record download ...
\textasciitilde{}>examples/hello\_world.srec
1 2 3 4 5 6 7 8 9 10 11 ...
{\lbrack}file transfer complete{\rbrack}
{\lbrack}connected{\rbrack}
\#\# Start Addr = 0x00040004
```

```
=> go 40004 Hello World! This is a test.
\#\# Starting application at 0x00040004 ...
Hello World
argc = 7
argv{\lbrack}0{\rbrack} = ''40004''
argv{\lbrack}1{\rbrack} = ''Hello''
argv{\lbrack}2{\rbrack} = ''World!''
argv{\lbrack}3{\rbrack} = ''This''
argv{\lbrack}4{\rbrack} = ''is''
argv{\lbrack}5{\rbrack} = ''a''
argv{\lbrack}6{\rbrack} = ''test.''
argv{\lbrack}7{\rbrack} = ''''
Hit any key to exit ...
```

```
\#\# Application terminated, rc = 0x0
```

Another example, which demonstrates how to register a CPM interrupt handler with the U-Boot code, can be found in 'examples/timer.c'. Here, a CPM timer is set up to generate an interrupt every second. The interrupt service routine is trivial, just printing a '.' character, but this is just a demo program. The application can be controlled by the following keys:

- ? - print current values of the CPM Timer registers
- b - enable interrupts and start timer
- e - stop timer and disable interrupts
- q - quit application

```
=> loads
\#\# Ready for S-Record download ...
\textasciitilde{}>examples/timer.srec
1 2 3 4 5 6 7 8 9 10 11 ...
{\lbrack}file transfer complete{\rbrack}
{\lbrack}connected{\rbrack}
\#\# Start Addr = 0x00040004
```

```
=> go 40004
\#\# Starting application at 0x00040004 ...
TIMERS=0xffff00980
Using timer 1
tgcr @ 0xffff00980, tmr @ 0xffff00990, trr @ 0xffff00994, tcr @ 0xffff00998, tcn @ 0xffff0099c, ter @ 0xffff009b0
```

```
Hit 'b':
{\lbrack}q, b, e, ?{\rbrack} Set interval 1000000 us
Enabling timer
Hit '?:
{\lbrack}q, b, e, ?{\rbrack} .....
tgcr=0x1, tmr=0xff1c, trr=0x3d09, tcr=0x0, tcn=0xef6, ter=0x0
Hit '?:
{\lbrack}q, b, e, ?{\rbrack} .
tgcr=0x1, tmr=0xff1c, trr=0x3d09, tcr=0x0, tcn=0x2ad4, ter=0x0
Hit '?:
{\lbrack}q, b, e, ?{\rbrack} .
tgcr=0x1, tmr=0xff1c, trr=0x3d09, tcr=0x0, tcn=0x1efc, ter=0x0
```



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```
Hit '?:
{\brack}q, b, e, ?{\rbrack} .
tocr=0x1, tmr=0xff1c, trr=0x3d09, tcr=0x0, tcn=0x169d, ter=0x0
Hit 'e':
{\brack}q, b, e, ?{\rbrack} ...Stopping timer
Hit 'q':
{\brack}q, b, e, ?{\rbrack} \#\# Application terminated, rc = 0x0
```

Minicom warning:  
=====

Over time, many people have reported problems when trying to use the 'minicom' terminal emulation program for serial download. I (wd) consider minicom to be broken, and recommend not to use it. Under Unix, I recommend to use C-Kermit for general purpose use (and especially for kermit binary protocol download ('loadb' command), and use 'cu' for S-Record download ('loads' command). See [https://www.denx.de/wiki/view/DULG/SystemSetup\#Section\\\_4.3](https://www.denx.de/wiki/view/DULG/SystemSetup\#Section\_4.3) for help with kermit.

Nevertheless, if you absolutely want to use it try adding this configuration to your 'File transfer protocols' section:

Name	Program	Name	U/D	FullScr	IO-Red.	Multi			
X	kermit	/usr/bin/kermit -i -l \%	l	-s	Y	U	Y	N	N
Y	kermit	/usr/bin/kermit -i -l \%	l	-r	N	D	Y	N	N

NetBSD Notes:  
=====

Starting at version 0.9.2, U-Boot supports NetBSD both as host (build U-Boot) and target system (boots NetBSD/mpc8xx).

Building requires a cross environment; it is known to work on NetBSD/i386 with the cross-powerpc-netbsd-1.3 package (you will also need gmake since the Makefiles are not compatible with BSD make). Note that the cross-powerpc package does not install include files; attempting to build U-Boot will fail because is missing. This file has to be installed and patched manually:

```
\# cd /usr/pkg/cross/powerpc-netbsd/include
\# mkdir powerpc
\# ln -s powerpc machine
\# cp /usr/src/sys/arch/powerpc/include/ansi.h powerpc/ansi.h
\# \${EDIT} powerpc/ansi.h \#\# must remove \_va\_list, \_BSD\_VA\_LIST
```

Native builds \*don't\* work due to incompatibilities between native and U-Boot include files.

Booting assumes that (the first part of) the image booted is a stage-2 loader which in turn loads and then invokes the kernel proper. Loader sources will eventually appear in the NetBSD source tree (probably in sys/arc/mpc8xx/stand/u-boot\\_stage2/); in the meantime, see [ftp://ftp.denx.de/pub/u-boot/ppcboot\\\_stage2.tar.gz](ftp://ftp.denx.de/pub/u-boot/ppcboot\_stage2.tar.gz)

Implementation Internals:  
=====

The following is not intended to be a complete description of every implementation detail. However, it should help to understand the inner workings of U-Boot and make it easier to port it to custom hardware.

Initial Stack, Global Data:  
-----

The implementation of U-Boot is complicated by the fact that U-Boot starts running out of ROM (flash memory), usually without access to system RAM (because the memory controller is not initialized yet). This means that we don't have writable Data or BSS segments, and BSS is not initialized as zero. To be able to get a C environment working at all, we have to allocate at least a minimal stack. Implementation



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options for this are defined and restricted by the CPU used: Some CPU models provide on-chip memory (like the IMMR area on MPC8xx and MPC826x processors), on others (parts of) the data cache can be locked as (mis-) used as memory, etc.

Chris Hallinan posted a good summary of these issues to the U-Boot mailing list:

Subject: RE: {\lbrack}U-Boot-Users{\rbrack} RE: More On Memory Bank x (nothingness)?  
From: ''Chris Hallinan''  
Date: Mon, 10 Feb 2003 16:43:46 -0500 (22:43 MET)  
...

Correct me if I'm wrong, folks, but the way I understand it is this: Using DCACHE as initial RAM for Stack, etc, does not require any physical RAM backing up the cache. The cleverness is that the cache is being used as a temporary supply of necessary storage before the SDRAM controller is setup. It's beyond the scope of this list to explain the details, but you can see how this works by studying the cache architecture and operation in the architecture and processor-specific manuals.

OCM is On Chip Memory, which I believe the 405GP has 4K. It is another option for the system designer to use as an initial stack/RAM area prior to SDRAM being available. Either option should work for you. Using CS 4 should be fine if your board designers haven't used it for something that would cause you grief during the initial boot! It is frequently not used.

CONFIG\\_SYS\\_INIT\\_RAM\\_ADDR should be somewhere that won't interfere with your processor/board/system design. The default value you will find in any recent u-boot distribution in walnut.h should work for you. I'd set it to a value larger than your SDRAM module. If you have a 64MB SDRAM module, set it above 400\\_0000. Just make sure your board has no resources that are supposed to respond to that address! That code in start.S has been around a while and should work as is when you get the config right.

-Chris Hallinan  
DS4.COM, Inc.

It is essential to remember this, since it has some impact on the C code for the initialization procedures:

\* Initialized global data (data segment) is read-only. Do not attempt to write it.

\* Do not use any uninitialized global data (or implicitly initialized as zero data - BSS segment) at all - this is undefined, initialization is performed later (when relocating to RAM).

\* Stack space is very limited. Avoid big data buffers or things like that.

Having only the stack as writable memory limits means we cannot use normal global data to share information between the code. But it turned out that the implementation of U-Boot can be greatly simplified by making a global data structure (gd\\_t) available to all functions. We could pass a pointer to this data as argument to \\_all\\_ functions, but this would bloat the code. Instead we use a feature of the GCC compiler (Global Register Variables) to share the data: we place a pointer (gd) to the global data into a register which we reserve for this purpose.

When choosing a register for such a purpose we are restricted by the relevant (E)ABI specifications for the current architecture, and by GCC's implementation.

For PowerPC, the following registers have specific use:

R1: stack pointer  
R2: reserved for system use  
R3-R4: parameter passing and return values  
R5-R10: parameter passing  
R13: small data area pointer  
R30: GOT pointer



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R31: frame pointer

(U-Boot also uses R12 as internal GOT pointer. r12 is a volatile register so r12 needs to be reset when going back and forth between asm and C)

==> U-Boot will use R2 to hold a pointer to the global data

Note: on PPC, we could use a static initializer (since the address of the global data structure is known at compile time), but it turned out that reserving a register results in somewhat smaller code - although the code savings are not that big (on average for all boards 752 bytes for the whole U-Boot image, 624 text + 127 data).

On ARM, the following registers are used:

R0: function argument word/integer result  
R1-R3: function argument word  
R9: platform specific  
R10: stack limit (used only if stack checking is enabled)  
R11: argument (frame) pointer  
R12: temporary workspace  
R13: stack pointer  
R14: link register  
R15: program counter

==> U-Boot will use R9 to hold a pointer to the global data

Note: on ARM, only R\_ARM\_RELATIVE relocations are supported.

On Nios II, the ABI is documented here:  
[https://www.altera.com/literature/hb/nios2/n2cpu\\_nii51016.pdf](https://www.altera.com/literature/hb/nios2/n2cpu_nii51016.pdf)

==> U-Boot will use gp to hold a pointer to the global data

Note: on Nios II, we give '-GO' option to gcc and don't use gp to access small data sections, so gp is free.

On NDS32, the following registers are used:

R0-R1: argument/return  
R2-R5: argument  
R15: temporary register for assembler  
R16: trampoline register  
R28: frame pointer (FP)  
R29: global pointer (GP)  
R30: link register (LP)  
R31: stack pointer (SP)  
PC: program counter (PC)

==> U-Boot will use R10 to hold a pointer to the global data

NOTE: DECLARE\_GLOBAL\_DATA\_PTR must be used with file-global scope, or current versions of GCC may 'optimize' the code too much.

On RISC-V, the following registers are used:

x0: hard-wired zero (zero)  
x1: return address (ra)  
x2: stack pointer (sp)  
x3: global pointer (gp)  
x4: thread pointer (tp)  
x5: link register (t0)  
x8: frame pointer (fp)  
x10-x11: arguments/return values (a0-1)  
x12-x17: arguments (a2-7)  
x28-31: temporaries (t3-6)  
pc: program counter (pc)

==> U-Boot will use gp to hold a pointer to the global data

Memory Management:

-----  
U-Boot runs in system state and uses physical addresses, i.e. the MMU is not used either for address mapping nor for memory protection.



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The available memory is mapped to fixed addresses using the memory controller. In this process, a contiguous block is formed for each memory type (Flash, SDRAM, SRAM), even when it consists of several physical memory banks.

U-Boot is installed in the first 128 kB of the first Flash bank (on TQM8xxL modules this is the range 0x40000000 ... 0x4001FFFF). After booting and sizing and initializing DRAM, the code relocates itself to the upper end of DRAM. Immediately below the U-Boot code some memory is reserved for use by malloc() {\lbrack}see CONFIG\\_SYS\\_MALLOC\\_LEN configuration setting{\rbrack}. Below that, a structure with global Board Info data is placed, followed by the stack (growing downward).

Additionally, some exception handler code is copied to the low 8 kB of DRAM (0x00000000 ... 0x00001FFF).

So a typical memory configuration with 16 MB of DRAM could look like this:

```
0x0000 0000 Exception Vector code
:
0x0000 1FFF
0x0000 2000 Free for Application Use
:
:
:
:
0x00FB FF20 Monitor Stack (Growing downward)
0x00FB FFAC Board Info Data and permanent copy of global data
0x00FC 0000 Malloc Arena
:
0x00FD FFFF
0x00FE 0000 RAM Copy of Monitor Code
... eventually: LCD or video framebuffer
... eventually: pRAM (Protected RAM - unchanged by reset)
0x00FF FFFF {\lbrack}End of RAM{\rbrack}
```

### System Initialization:

-----

In the reset configuration, U-Boot starts at the reset entry point (on most PowerPC systems at address 0x00000100). Because of the reset configuration for CS0\# this is a mirror of the on board Flash memory. To be able to re-map memory U-Boot then jumps to its link address. To be able to implement the initialization code in C, a (small!) initial stack is set up in the internal Dual Ported RAM (in case CPUs which provide such a feature like), or in a locked part of the data cache. After that, U-Boot initializes the CPU core, the caches and the SIU.

Next, all (potentially) available memory banks are mapped using a preliminary mapping. For example, we put them on 512 MB boundaries (multiples of 0x20000000: SDRAM on 0x00000000 and 0x20000000, Flash on 0x40000000 and 0x60000000, SRAM on 0x80000000). Then UPM A is programmed for SDRAM access. Using the temporary configuration, a simple memory test is run that determines the size of the SDRAM banks.

When there is more than one SDRAM bank, and the banks are of different size, the largest is mapped first. For equal size, the first bank (CS2\#) is mapped first. The first mapping is always for address 0x00000000, with any additional banks following immediately to create contiguous memory starting from 0.

Then, the monitor installs itself at the upper end of the SDRAM area and allocates memory for use by malloc() and for the global Board Info data; also, the exception vector code is copied to the low RAM pages, and the final stack is set up.

Only after this relocation will you have a 'normal' C environment; until that you are restricted in several ways, mostly because you are running from ROM, and because the code will have to be relocated to a new address in RAM.



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### U-Boot Porting Guide:

-----  
{\lbrack}Based on messages by Jerry Van Baren in the U-Boot-Users mailing list, October 2002{\rbrack}

```
int main(int argc, char *argv{\lbrack}{\rbrack})
\{
sighandler\_t no\_more\_time;
```

```
signal(SIGALRM, no\_more\_time);
alarm(PROJECT\_DEADLINE - toSec (3 * WEEK));
```

```
if (available\_money > available\_manpower) \{
Pay consultant to port U-Boot;
return 0;
\}
```

Download latest U-Boot source;

Subscribe to u-boot mailing list;

```
if (clueless)
email(''Hi, I am new to U-Boot, how do I get started?'');
```

```
while (learning) \{
Read the README file in the top level directory;
Read https://www.denx.de/wiki/bin/view/DULG/Manual;
Read applicable doc/README.*;
Read the source, Luke;
/* find . -name '*.{\lbrack}chS{\rbrack}' | xargs grep -i */
\}
```

```
if (available\_money > toLocalCurrency (\$2500))
Buy a BDI3000;
else
Add a lot of aggravation and time;
```

```
if (a similar board exists) \{ /* hopefully... */
cp -a board/ board/
cp include/configs/.h include/configs/.h
\} else \{
Create your own board support subdirectory;
Create your own board include/configs/.h file;
\}
Edit new board/ files
Edit new include/configs/.h
```

```
while (!accepted) \{
while (!running) \{
do \{
Add / modify source code;
\} until (compiles);
Debug;
if (clueless)
email(''Hi, I am having problems...'');
\}
Send patch file to the U-Boot email list;
if (reasonable critiques)
Incorporate improvements from email list code review;
else
Defend code as written;
\}
```

```
return 0;
\}
```

```
void no\_more\_time (int sig)
\{
hire\_a\_guru();
\}
```

### Coding Standards:

-----





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All contributions to U-Boot should conform to the Linux kernel coding style; see the kernel coding style guide at <https://www.kernel.org/doc/html/latest/process/coding-style.html>, and the script `'scripts/Lindent'` in your Linux kernel source directory.

Source files originating from a different project (for example the MTD subsystem) are generally exempt from these guidelines and are not reformatted to ease subsequent migration to newer versions of those sources.

Please note that U-Boot is implemented in C (and to some small parts in Assembler); no C++ is used, so please do not use C++ style comments (`//`) in your code.

Please also stick to the following formatting rules:

- remove any trailing white space
- use TAB characters for indentation and vertical alignment, not spaces
- make sure NOT to use DOS `'\textbackslash{}r\textbackslash{}n'` line feeds
- do not add more than 2 consecutive empty lines to source files
- do not add trailing empty lines to source files

Submissions which do not conform to the standards may be returned with a request to reformat the changes.

Submitting Patches:

-----

Since the number of patches for U-Boot is growing, we need to establish some rules. Submissions which do not conform to these rules may be rejected, even when they contain important and valuable stuff.

Please see <https://www.denx.de/wiki/U-Boot/Patches> for details.

Patches shall be sent to the u-boot mailing list ;  
see <https://lists.denx.de/listinfo/u-boot>

When you send a patch, please include the following information with it:

- \* For bug fixes: a description of the bug and how your patch fixes this bug. Please try to include a way of demonstrating that the patch actually fixes something.
- \* For new features: a description of the feature and your implementation.
- \* For major contributions, add a MAINTAINERS file with your information and associated file and directory references.
- \* When you add support for a new board, don't forget to add a maintainer e-mail address to the boards.cfg file, too.
- \* If your patch adds new configuration options, don't forget to document these in the README file.
- \* The patch itself. If you are using git (which is *strongly* recommended) you can easily generate the patch using the `'git format-patch'`. If you then use `'git send-email'` to send it to the U-Boot mailing list, you will avoid most of the common problems with some other mail clients.

If you cannot use git, use `'diff -purN OLD NEW'`. If your version of diff does not support these options, then get the latest version of GNU diff.

The current directory when running this command shall be the parent directory of the U-Boot source tree (i. e. please make sure that your patch includes sufficient directory information for the affected files).

We prefer patches as plain text. MIME attachments are discouraged, and compressed attachments must not be used.

- \* If one logical set of modifications affects or creates several files, all these changes shall be submitted in a SINGLE patch file.



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\* Changesets that contain different, unrelated modifications shall be submitted as SEPARATE patches, one patch per changeset.

### Notes:

\* Before sending the patch, run the buildman script on your patched source tree and make sure that no errors or warnings are reported for any of the boards.

\* Keep your modifications to the necessary minimum: A patch containing several unrelated changes or arbitrary reformatting will be returned with a request to re-formatting / split it.

\* If you modify existing code, make sure that your new code does not add to the memory footprint of the code ;-) Small is beautiful! When adding new features, these should compile conditionally only (using `\#ifdef`), and the resulting code with the new feature disabled must not need more memory than the old code without your modification.

\* Remember that there is a size limit of 100 kB per message on the u-boot mailing list. Bigger patches will be moderated. If they are reasonable and not too big, they will be acknowledged. But patches bigger than the size limit should be avoided.

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Licenses of module/program 'abe-2be48683f5299fe1bca93b9d2f1816862a4ffbb3':

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Version 3, 29 June 2007

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